

GA-MA69G-S3H

SHEET TITLE

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02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	AM2 HT INTERFACE
05	AM2 DDR2 INTERFACE
06	AM2 CONTROL & DEBUG
07	AM2 POWER
08	DIMM 1,2
09	DIMM 3,4
10	DIMM TERMINATION
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12	ATI RS690G R.G.B. DAC
13	ATI RS690G PCIE
14	ATI RS690G POWER
15	RGB & TV Connect
16	ICS 951464AG
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18	ATI SB600-ACPI/USB
19	ATI SB600-SATA/IDE
20	ATI SB600-POWER
21	ITE 8716GB/CX ,SPI BIOS
22	PCI EXPRESS X16, X4 SLOT
23	COM,LPT,USB PORT
24	PCI 1,2,SLOT
25	CODEC ALC889A F_AUDIO

SHEET TITLE

Revision:1.0

26	AUDIO JACK
27	H/W MONITOR & FAN CONTROL
28	IDE/FDD ,HDMI
29	PWM ISL6312
30	VCC12_DUAL,VCC12_HT,VDDA25 POWER
31	POWER SEQUENCE
32	VCC12 & DDRII POWER
33	FRONT PANEL & ATX POWER CONNCTOR
34	LAN RTL8110SC
35	TI TSB43AB23 1394a

GIGABYTE			
Title COVER SHEET			
Size Custom	Document Number GA-MA69G-S3H		Rev 1.0
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Component value change history

Version: 1.0

P-Code: U95045-0

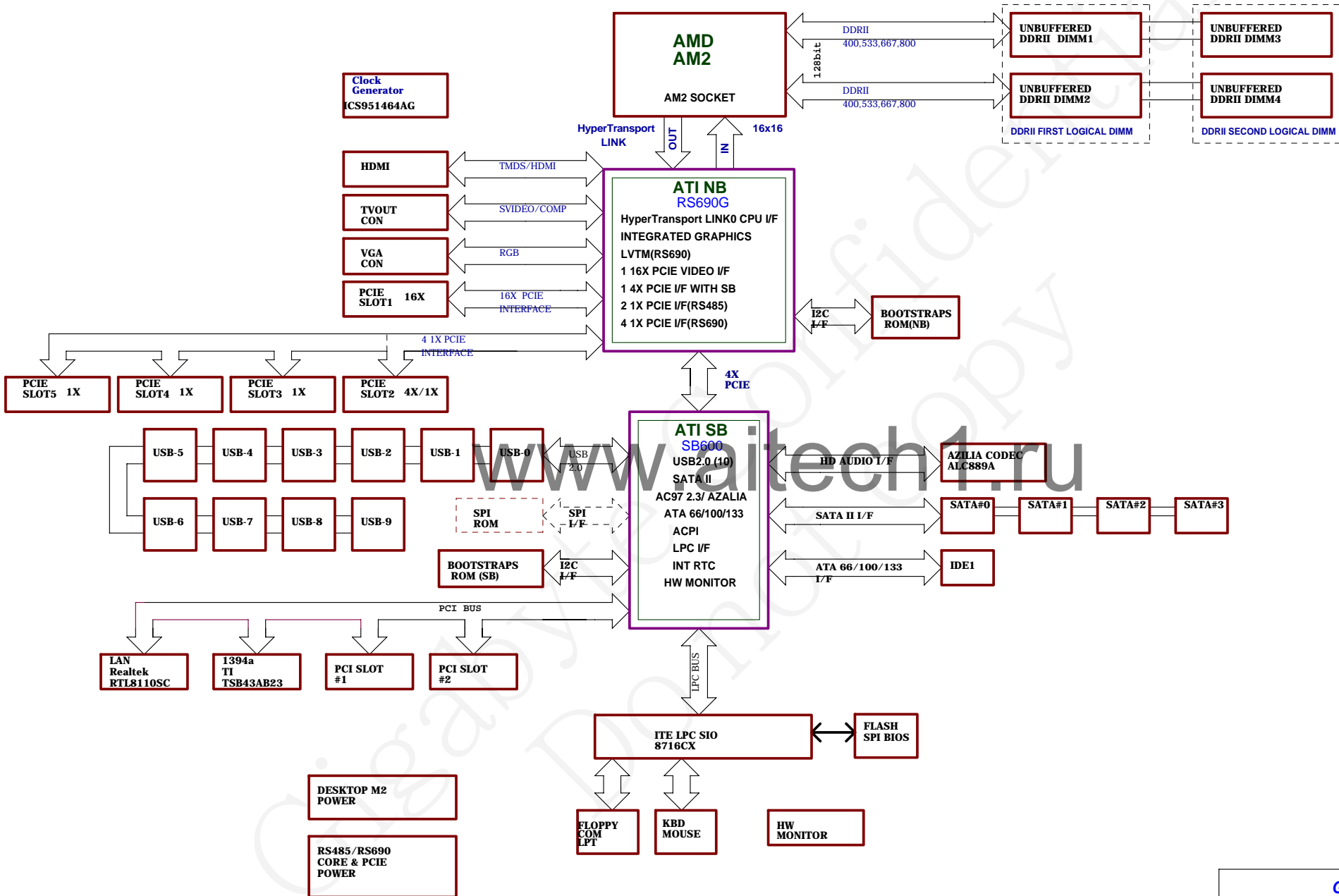
[illegible]

Circuit or PCB layout change for next version

[illegible]

BLOCK DIAGRAM

MORAY RS690 CUSTOMER DESKTOP REFERENCE DESIGN



GIGABYTE

BLOCK DIAGRAM

Size	Document Number	Rev
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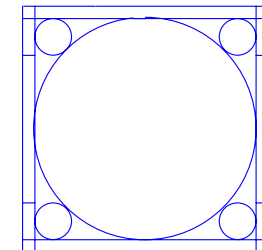
L0_CADIN_L[0..15] <L0_CADIN_L[0..15] 11
L0_CADIN_H[0..15] <L0_CADIN_H[0..15] 11
L0_CLKIN_L[0..1] <L0_CLKIN_L[0..1] 11
L0_CLKIN_H[0..1] <L0_CLKIN_H[0..1] 11
L0_CADOUT_L[0..15] <L0_CADOUT_L[0..15] 11
L0_CADOUT_H[0..15] <L0_CADOUT_H[0..15] 11
L0_CLKOUT_L[0..1] <L0_CLKOUT_L[0..1] 11
L0_CLKOUT_H[0..1] <L0_CLKOUT_H[0..1] 11

CPU_VDD_RUN = VCORE
CPU_VDDA_RUN = VDDA25
VLDT_RUN = VCC12_HT
CPU_VDDIO_SUS = DDR18V
CPU_VTT_SUS = DDRVTT

VLDT_A = VCC12_HT
VLDT_B = HT12B

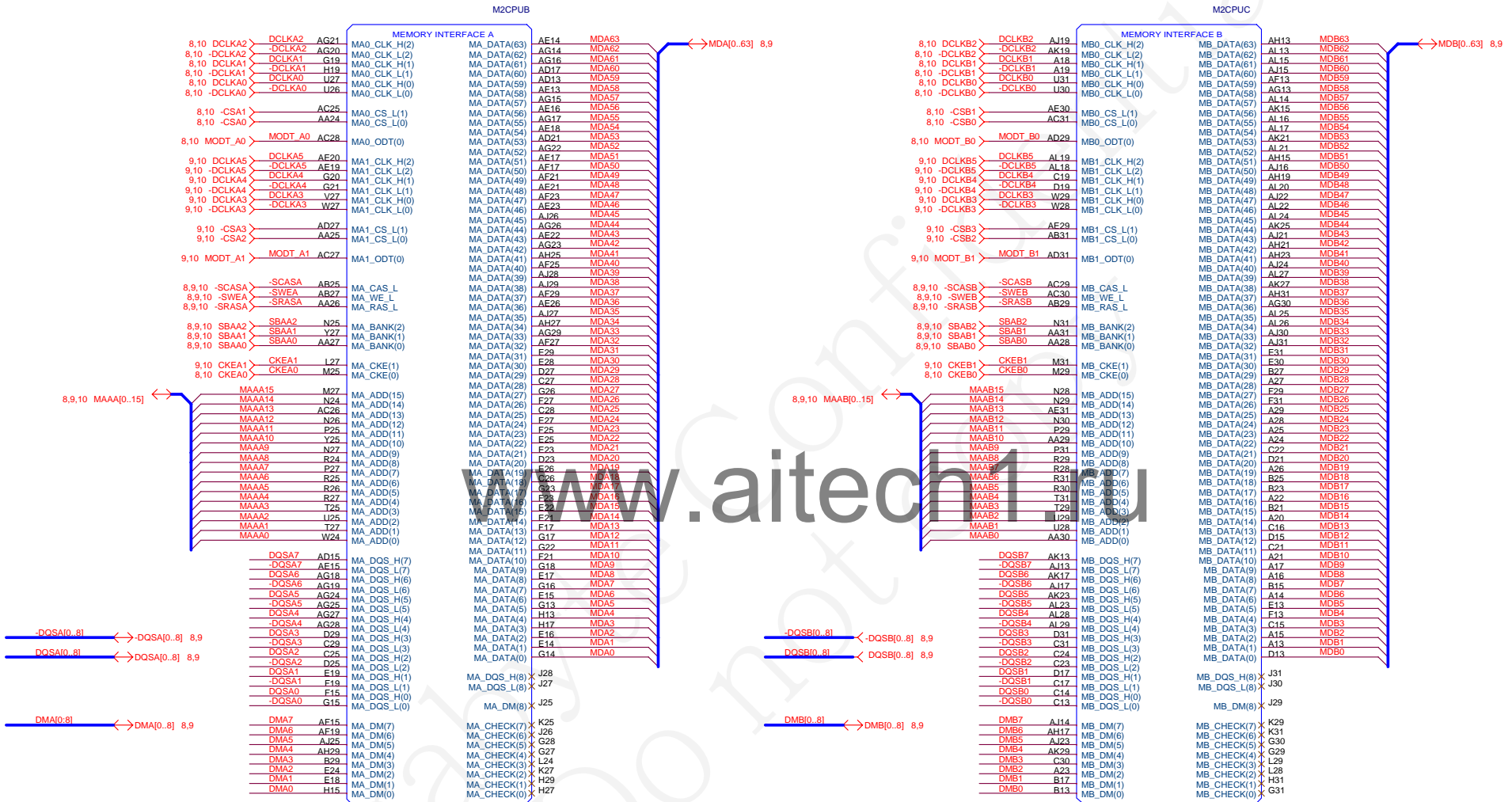


SOCKET_M2
AM2RM/PP/OR/PB[12KRC-04K807-42R_12KRC-04K807-43R]



GIGABYTE

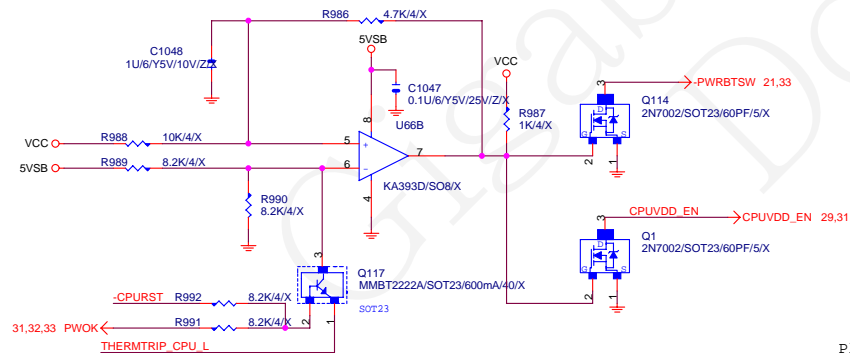
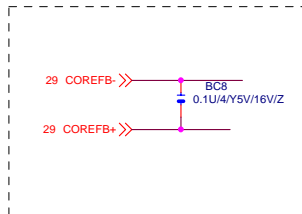
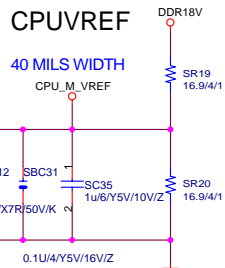
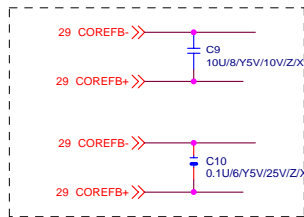
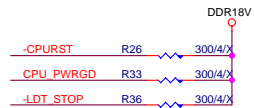
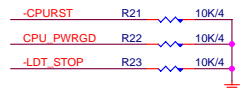
Title			
CPU HYPER TRANSPORT			
Size	Document Number	Rev	
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GIGABYTE

CPU DDRII MEMORY

Title		Rev
Size		1.0
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RS3 CLOSE CPU VR MOSFET

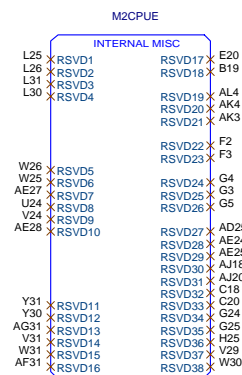
asserted at 131 degree
deasserted at 116 degree

HR100K(10RH2-001003-21)
Plase at PH3 copper

LAYOUT: Route trace 50 mils wide and
500 to 750 mils long between these caps.

Erratum 133, Revision Guide for
AMD NPT 0Fh Processors

Erratum 133, Revision Guide for
AMD NPT 0Fh Processors



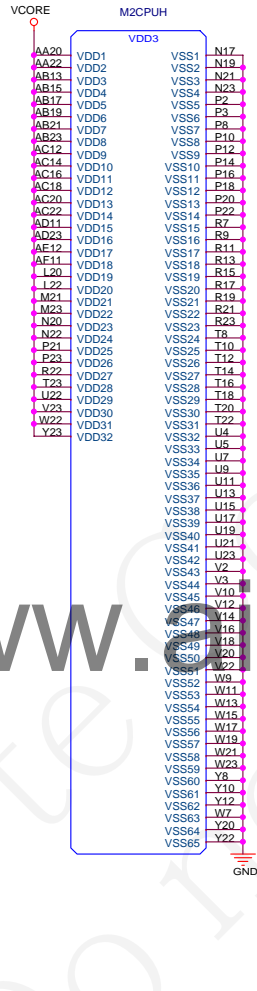
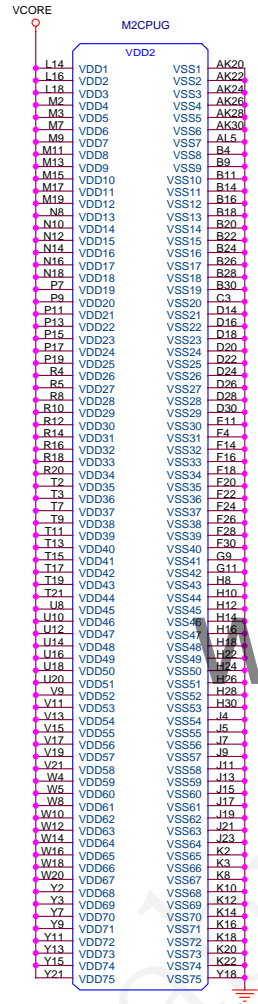
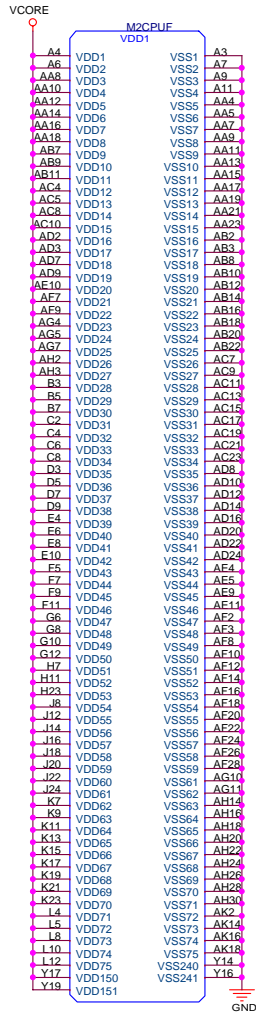
GIGABYTE

CPU CONTROL

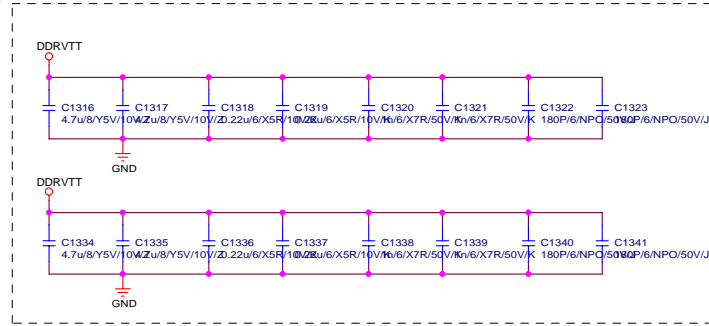
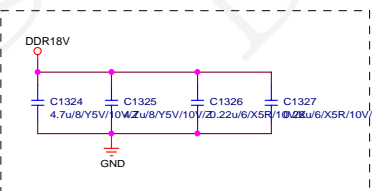
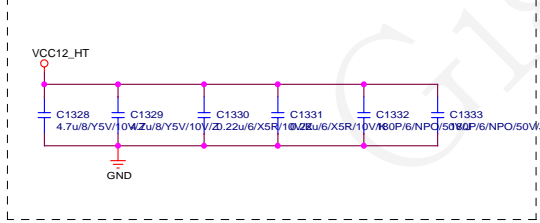
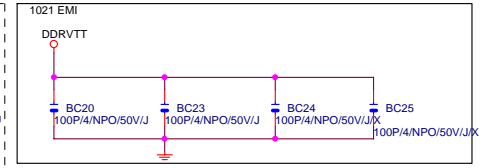
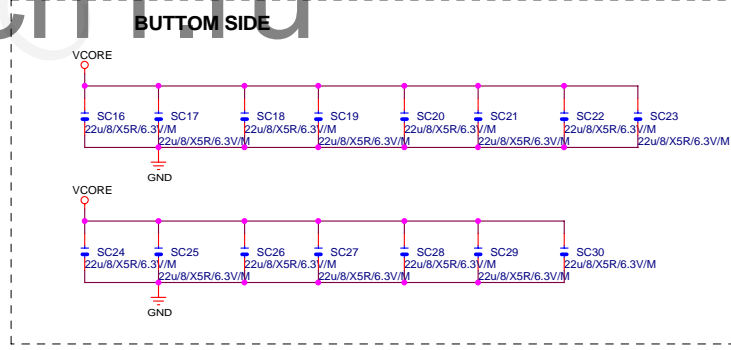
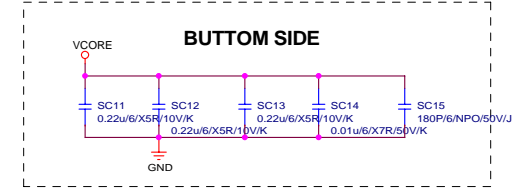
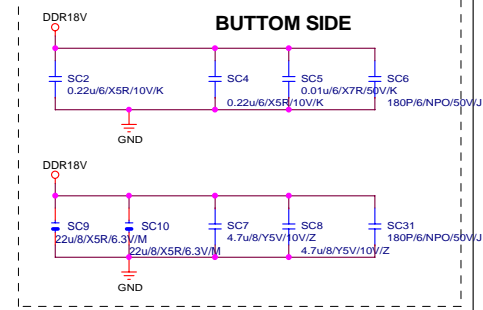
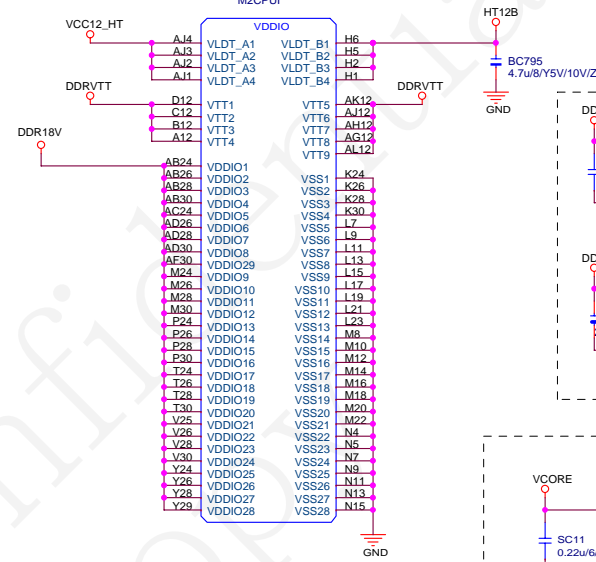
GA-MA69G-S3H

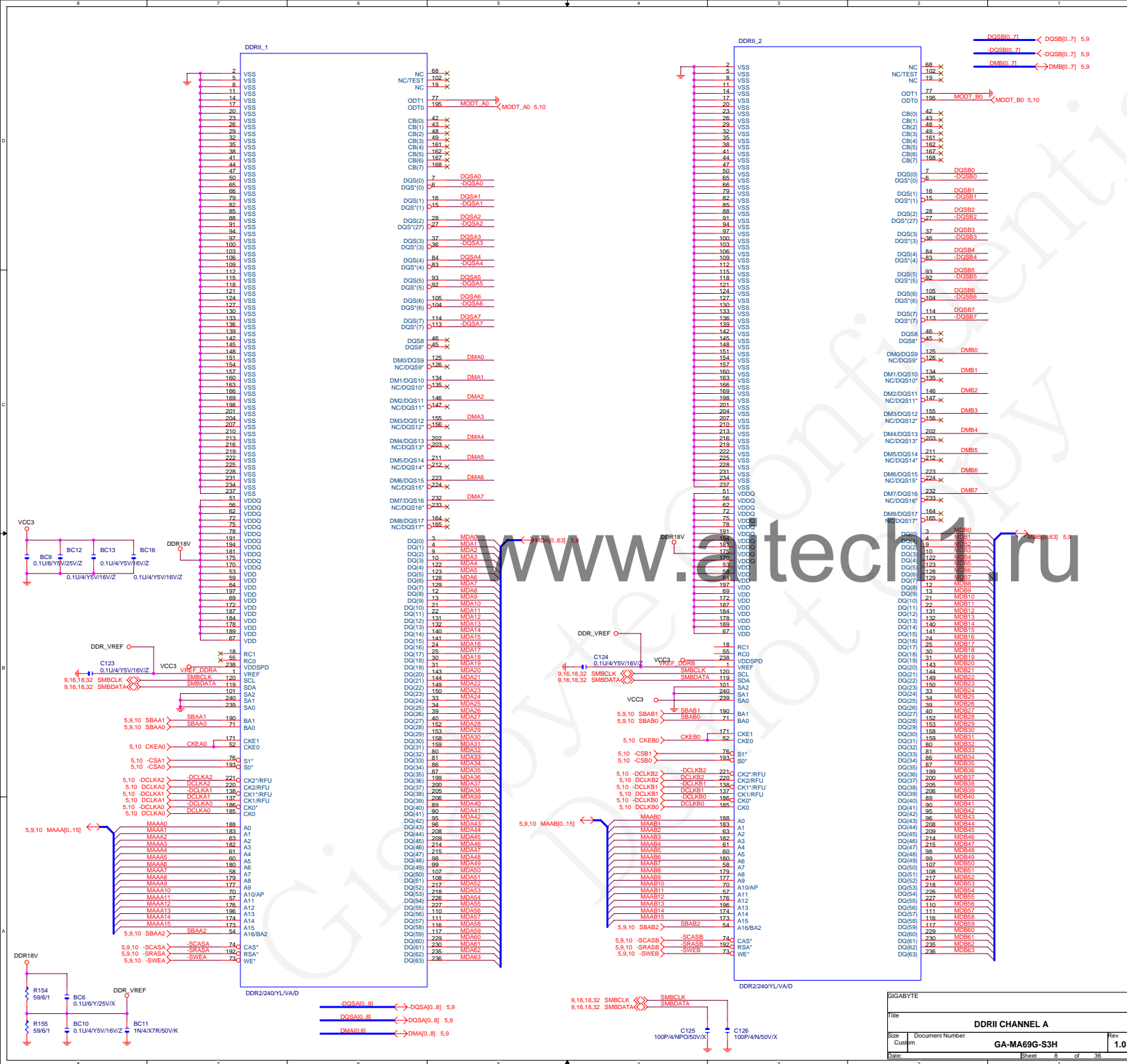
Rev 1.0

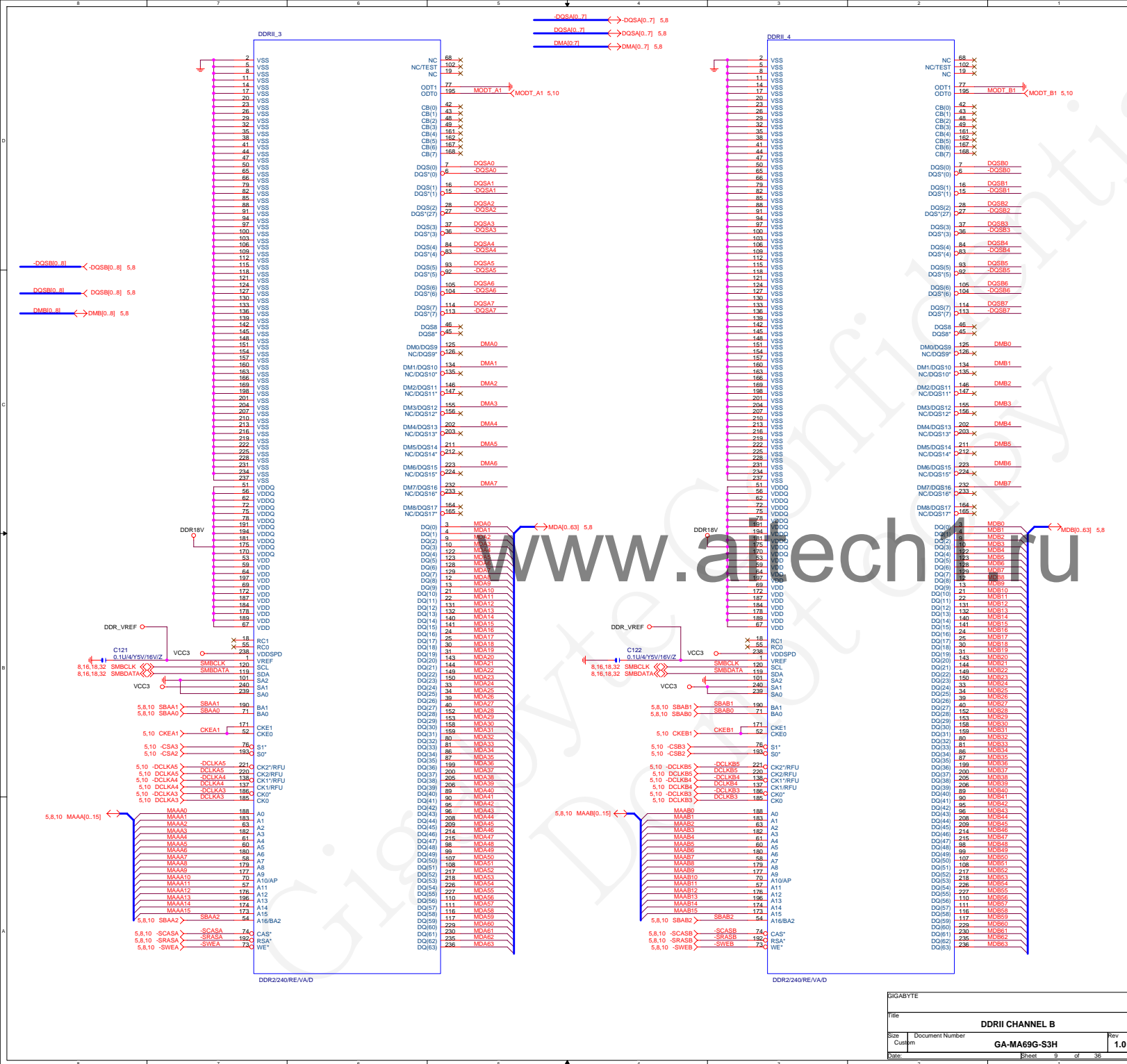
Date: Thursday, April 19, 2007 Sheet 6 of 36

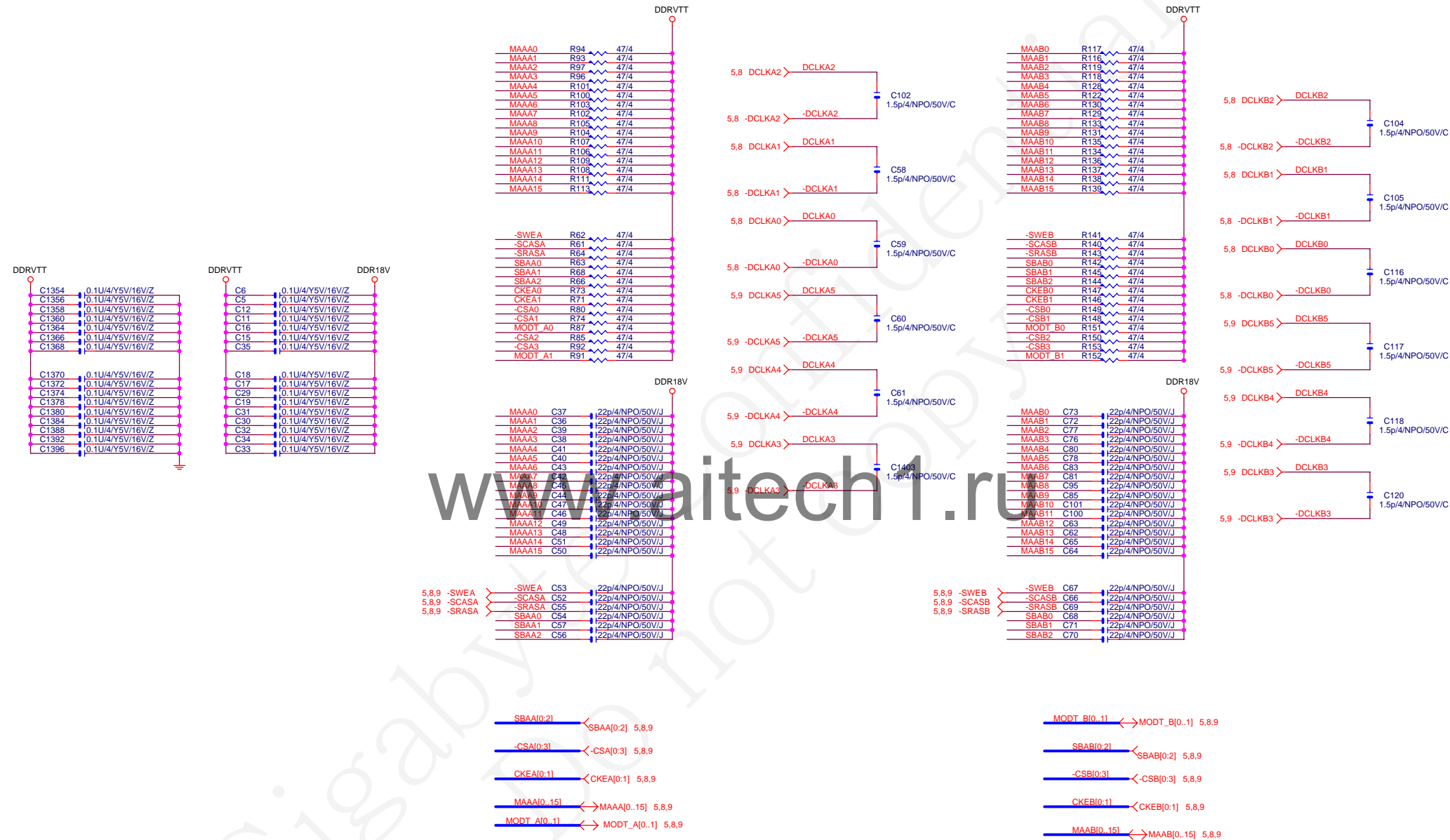


VLDT_RUN_B is connected to the VLDT_RUN power supply through the package or on the die. It is only connected on the board to decoupling near the CPU package.



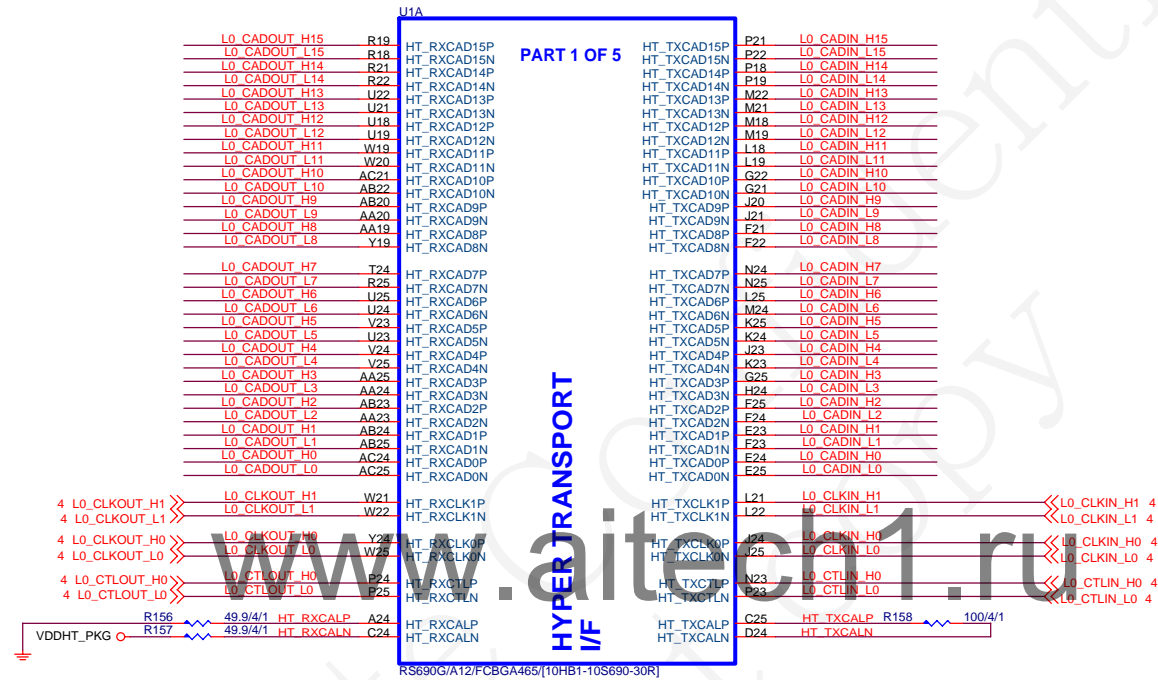




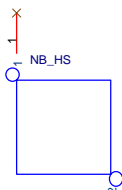


4 L0_CADOUT_H[0..15] < L0_CADOUT_H[0..15]
4 L0_CADOUT_L[0..15] < L0_CADOUT_L[0..15]

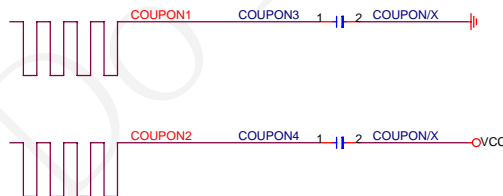
L0_CADIN_H[0..15] > L0_CADIN_H[0..15] 4
L0_CADIN_L[0..15] > L0_CADIN_L[0..15] 4



N.B HEATSINK

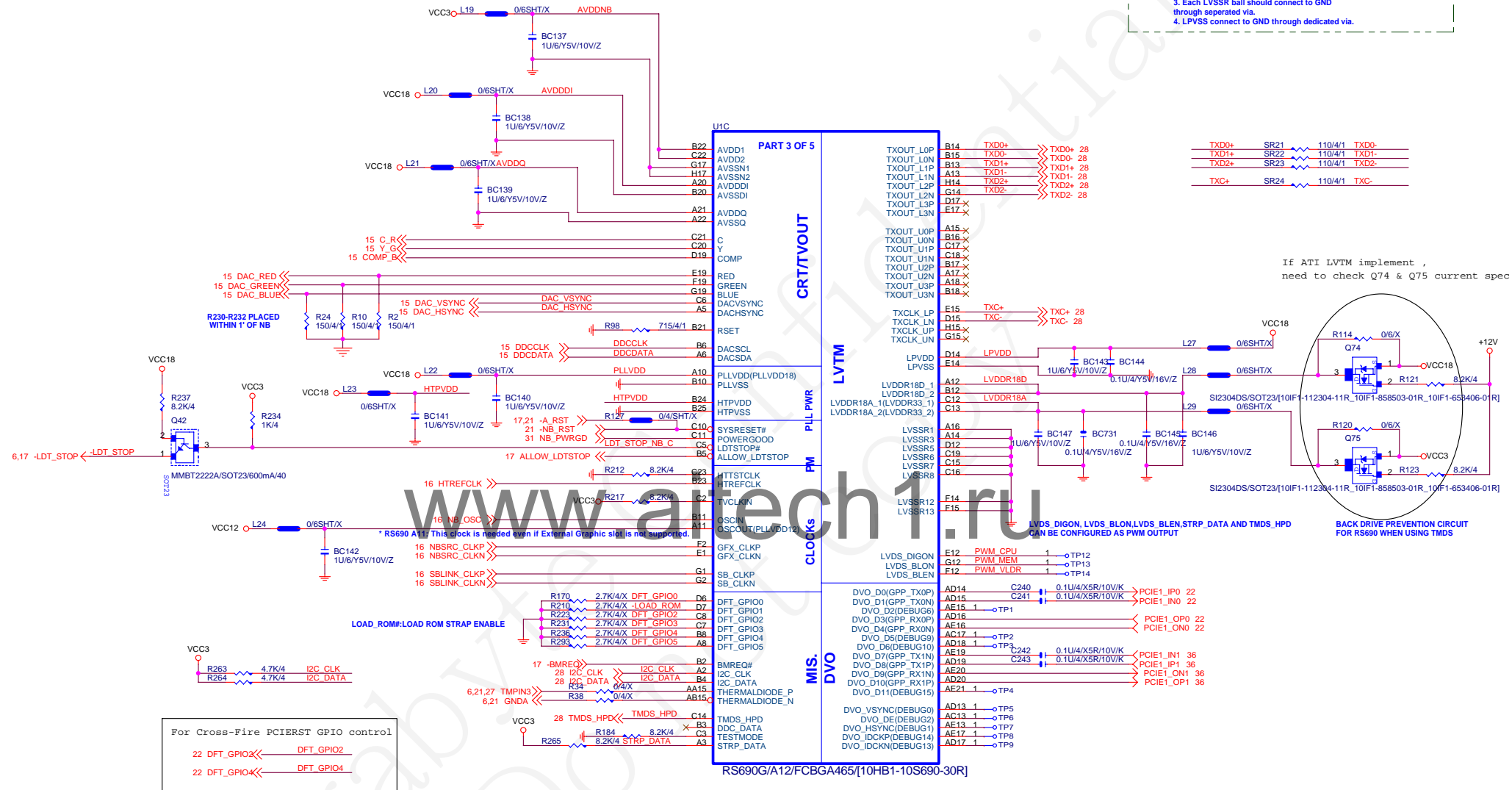


BGASINK_NB[12SP2-04E004-51R_12SP2-04E004-52R_12SP2-04E004-53R]



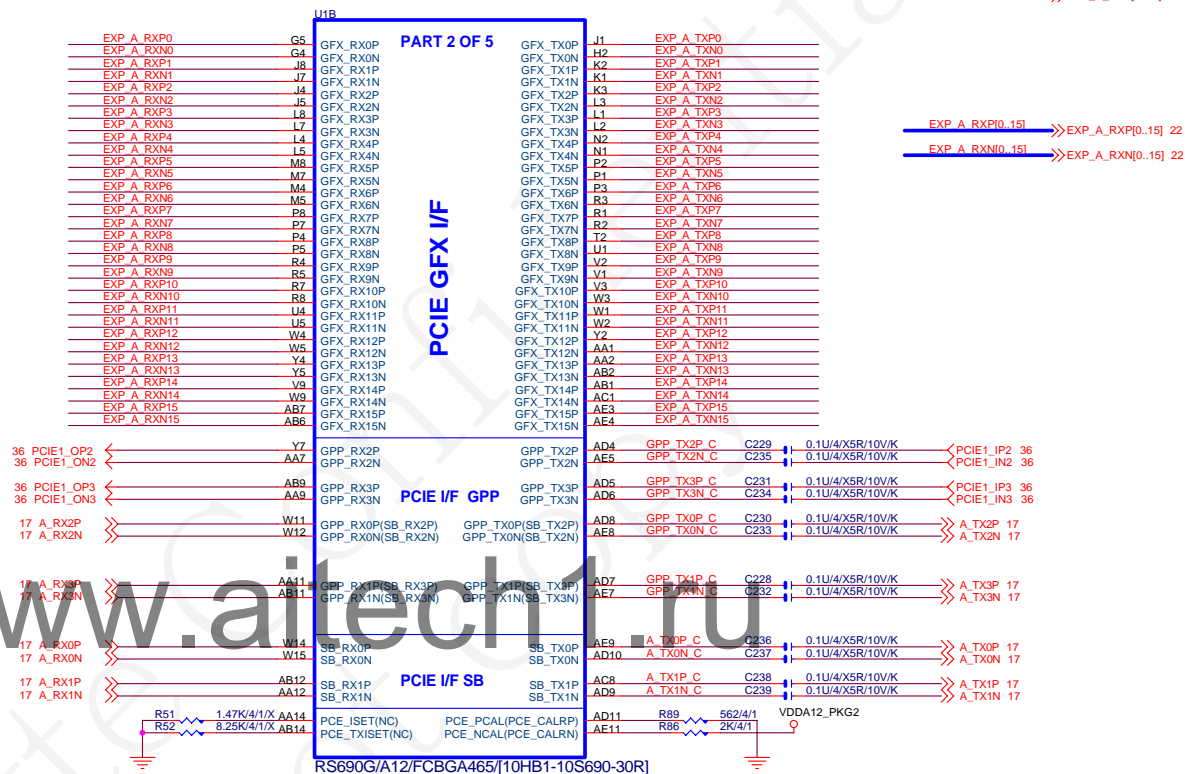
GIGABYTE

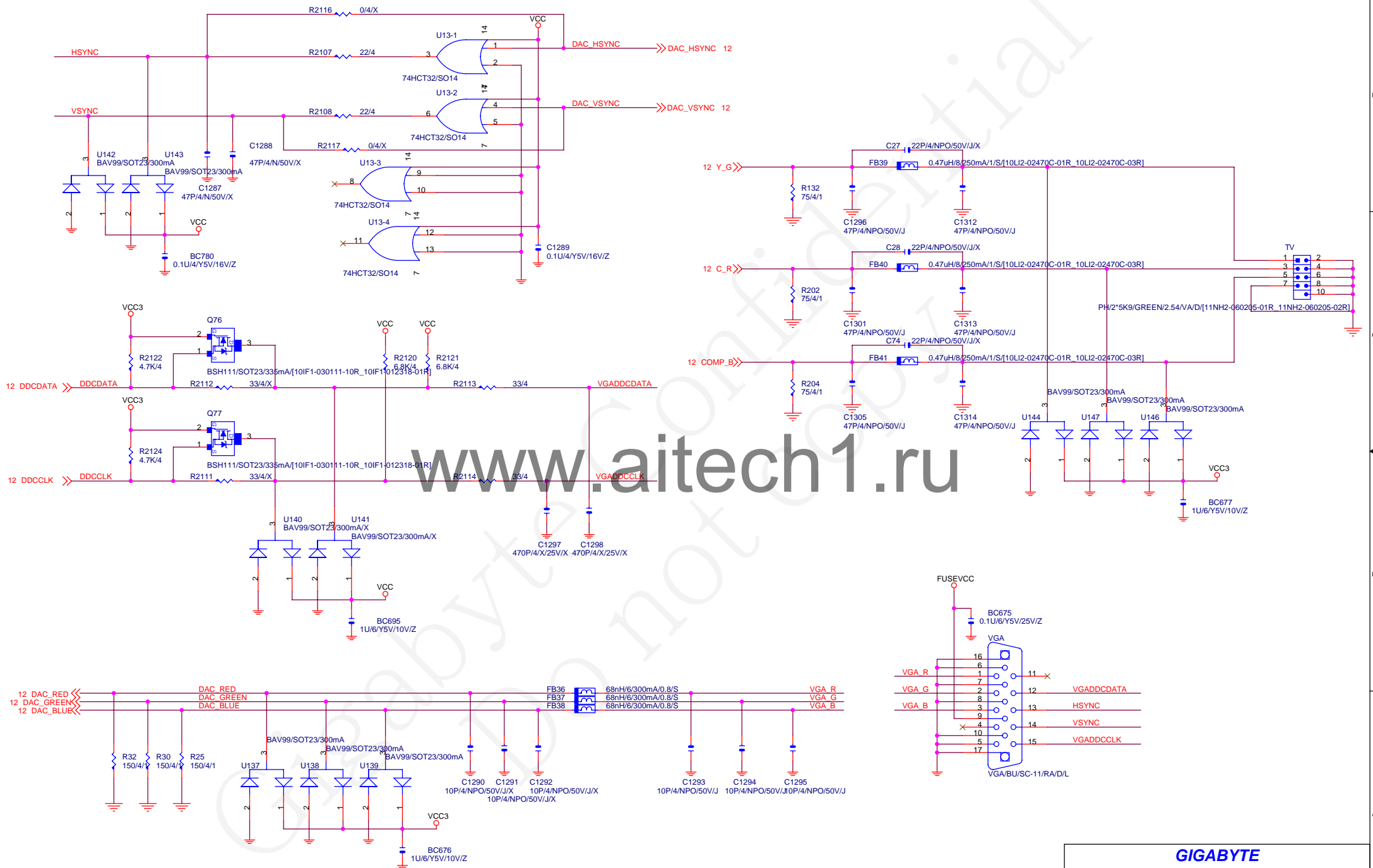
Title			
ATI RS690G-HOST			
Size	Document Number	Rev	
Custom	GA-MA69G-S3H	1.0	
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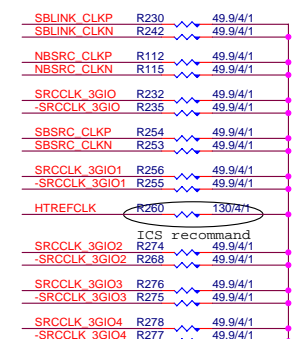
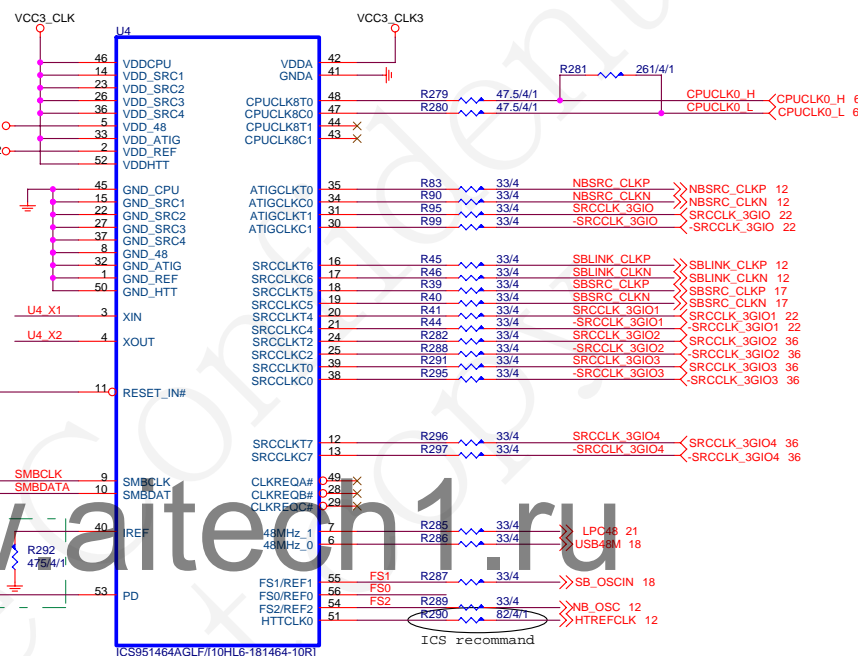
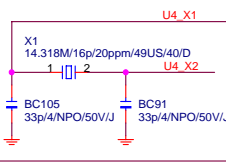
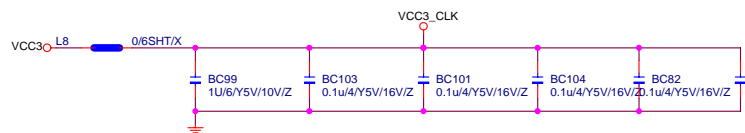
	RS690			
	RS690 only (NC for RS485)			
	DFT_GPIO1	DFT_GPIO0	DFT_GPIO[4:2]	DFT_GPIO5
PULL HIGH (internally pulled high)	Bypass the loading of EEPROM straps and use Hardware default values DEFAULT	Memory side port not available DEFAULT	These pin straps are used to configure PCI-E GPP mode: 111: register defined (register default to Config E) DEFAULT 110: 4-0-0-0 Config A 101: 4-4 Config B 100: 4-2-2 Config C 011: 4-2-1-1 Config D 010: 4-1-1-1-1 Config E others: register defined (register default to Config E)	Enable debug bus via the memory IO pads, if available in the package use default values DEFAULT
PULL LOW	I2C Master can load strap values from EEPROM if connected, or use default values if not connected	Memory side port available		use the memory data bus to output the debug bus

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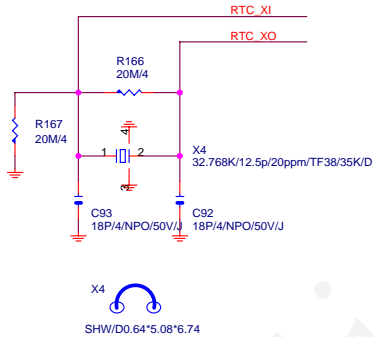
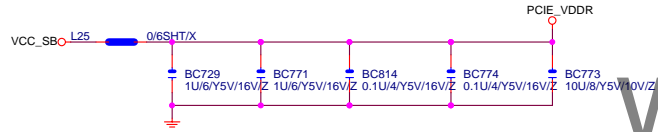
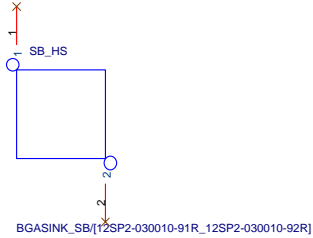
GIGABYTE			
Title			
RGB CONNECT			
Size	Document Number	Rev	
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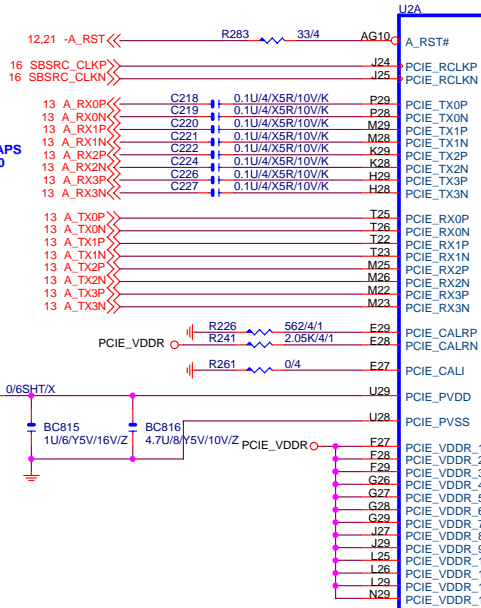
FS2	FS1	FS0	CPU	SRCCLK [2:1]	HTT	PCI	USB	COMMENT
0	0	0	Hi-Z	100.00	Hi-Z	Hi-Z	48.00	Reserved
0	0	1	X	100.00	X/3	X/6	48.00	Reserved
0	1	0	180.00	100.00	60.00	30.00	48.00	Reserved
0	1	1	220.00	100.00	36.56	73.12	48.00	Reserved
1	0	0	100.00	100.00	66.66	33.33	48.00	Reserved
1	0	1	133.33	100.00	66.66	33.33	48.00	Reserved
1	1	1	200.00	100.00	66.66	33.33	48.00	Normal ATHLON64 operation

SB CALIBRATION RESISTOR VALUE	
SB600	
R601	562 OHM 1%
R602	2.05K 1%
R603	0R

S.B HEATSINK



PLACE PCIE CAPS
CLOSE TO U600



SB600 SB 23x23mm

Part 1 of 4

PCI EXPRESS INTERFACE

PCI INTERFACE

PCI CLK

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SP TX2P C C1279 0.01U/4/X7R/16V/K
SP TX2M C C1278 0.01U/4/X7R/16V/K
SP RX2M C C1282 0.01U/4/X7R/16V/K
SP RX2P C C1283 0.01U/4/X7R/16V/K

SATAII1

SATAII3

SATAII2_3
SATA27/YLH/P/NA/D/2/B/GBT



PLACE SATA AC COUPLING
CAPS CLOSE TO SB600

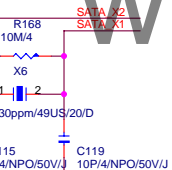
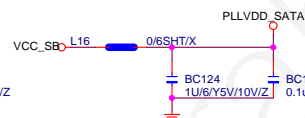
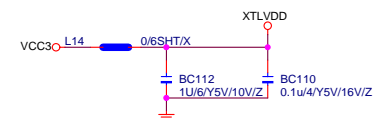
SP TX3P C C1280 0.01U/4/X7R/16V/K
SP TX3M C C1281 0.01U/4/X7R/16V/K
SP RX3M C C1285 0.01U/4/X7R/16V/K
SP RX3P C C1284 0.01U/4/X7R/16V/K

SATAII0

SATAII2

SATAII0_1
SATA27/YLH/P/NA/D/2/B/GBT

SP TX1P C C1286 0.01U/4/X7R/16V/K
SP TX1M C C1300 0.01U/4/X7R/16V/K
SP RX1M C C1311 0.01U/4/X7R/16V/K
SP RX1P C C1307 0.01U/4/X7R/16V/K



SP TX0P C AH21 SATA_TX0+
SP TX0M C AJ21 SATA_TX0-
SP RX0M C AH20 SATA_RX0-
SP RX0P C AJ20 SATA_RX0+
SP TX1P C AH18 SATA_TX1+
SP TX1M C AJ18 SATA_TX1-
SP RX1M C AH17 SATA_RX1-
SP RX1P C AJ17 SATA_RX1+

SP TX2P C AH13 SATA_TX2+
SP TX2M C AH14 SATA_TX2-
SP RX2M C AH16 SATA_RX2-
SP RX2P C AJ16 SATA_RX2+

SP TX3P C AJ11 SATA_TX3+
SP TX3M C AH11 SATA_TX3-
SP RX3M C AH12 SATA_RX3-
SP RX3P C AJ13 SATA_RX3+

SP RX3M C AH12 SATA_RX3-
SP RX3P C AJ13 SATA_RX3+

SATA_X1 AD16 SATA_X1
SATA_X2 AD18 SATA_X2

SATA_X1 AD16 SATA_X1
SATA_X2 AD18 SATA_X2

SATA_X1 AD16 SATA_X1
SATA_X2 AD18 SATA_X2

SATA_X1 AD16 SATA_X1
SATA_X2 AD18 SATA_X2

SATA_X1 AD16 SATA_X1
SATA_X2 AD18 SATA_X2

SATA_X1 AD16 SATA_X1
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SATA_X2 AD18 SATA_X2

SATA_X1 AD16 SATA_X1
SATA_X2 AD18 SATA_X2

SATA_X1 AD16 SATA_X1
SATA_X2 AD18 SATA_X2

SATA_X1 AD16 SATA_X1
SATA_X2 AD18 SATA_X2

SATA_X1 AD16 SATA_X1
SATA_X2 AD18 SATA_X2

SATA_X1 AD16 SATA_X1
SATA_X2 AD18 SATA_X2

SB600 SB 23x23mm

Part 2 of 4

SERIAL ATA

SERIAL ATA POWER

SPIROM

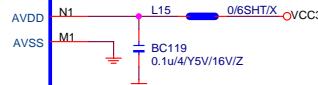
HW MONITOR

IDE_IORDY AB29 PIORDY
IDE_IRQ AA28 IRQ14
IDE_A0 AA29 PDA0
IDE_A1 AB27 PDA1
IDE_A2 Y28 PDA2
IDE_DACK# AB28 -PDDACK
IDE_DRQ AC27 PDDREQ
IDE_IOR# AC29 PDIOR
IDE_IOW# AC28 PDIOV
IDE_CS1# W28 -PCS1
IDE_CS3# W27 -PCS3
IDE_D0/GPIO15 AD28 PDD0
IDE_D1/GPIO16 AD26 PDD1
IDE_D2/GPIO17 AE29 PDD2
IDE_D3/GPIO18 AF27 PDD3
IDE_D4/GPIO19 AG29 PDD4
IDE_D5/GPIO20 AH28 PDD5
IDE_D6/GPIO21 AJ27 PDD6
IDE_D7/GPIO22 AH27 PDD7
IDE_D8/GPIO23 AG27 PDD8
IDE_D9/GPIO24 AE28 PDD9
IDE_D10/GPIO25 AF28 PDD10
IDE_D11/GPIO26 AE29 PDD11
IDE_D12/GPIO27 AE28 PDD12
IDE_D13/GPIO28 AD25 PDD13
IDE_D14/GPIO29 AD29 PDD14
IDE_D15/GPIO30 AD29 PDD15

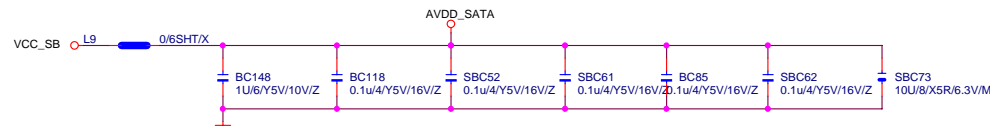
SPIROM

HW MONITOR

SPI_DI/GPIO12 J3 1 TP31
SPI_DO/GPIO11 J6 1 TP32
SPI_CLK/GPIO47 G3 1 TP33
SPI_HOLD/GPIO31 G2 1 TP34
SPI_CS#/GPIO32 G6 1 TP35
LAN_RST#/GPIO13 C23 -SB600 LAN_RST 1 TP29
ROM_RST#/GPIO14 G5 -SB600 ROM_RST 1 TP30
FANOUT0/GPIO3 M4 X
FANOUT1/GPIO48 T3 X
FANOUT2/GPIO49 V4 X
FANIN0/GPIO50 N3 X
FANIN1/GPIO51 P2 X
FANIN2/GPIO52 V4 X
TEMP_COMM P5 X
TEMPIN0/GPIO61 P7 X
TEMPIN1/GPIO62 P8 X
TEMPIN2/GPIO63 T8 X
TEMPIN3/TALERT#/GPIO64 T7 X
VIN0/GPIO53 V5 X
VIN1/GPIO54 L7 X
VIN2/GPIO55 M8 X
VIN3/GPIO56 V8 X
VIN4/GPIO57 M6 X
VIN5/GPIO58 P4 X
VIN6/GPIO59 M7 X
VIN7/GPIO60 V7 X



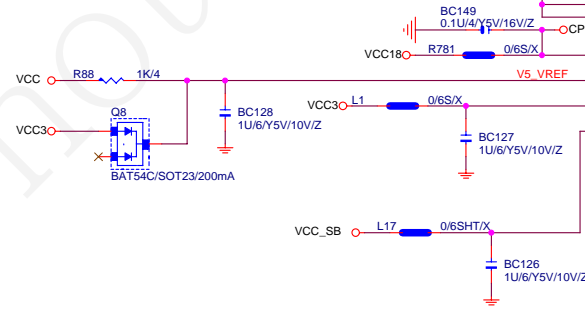
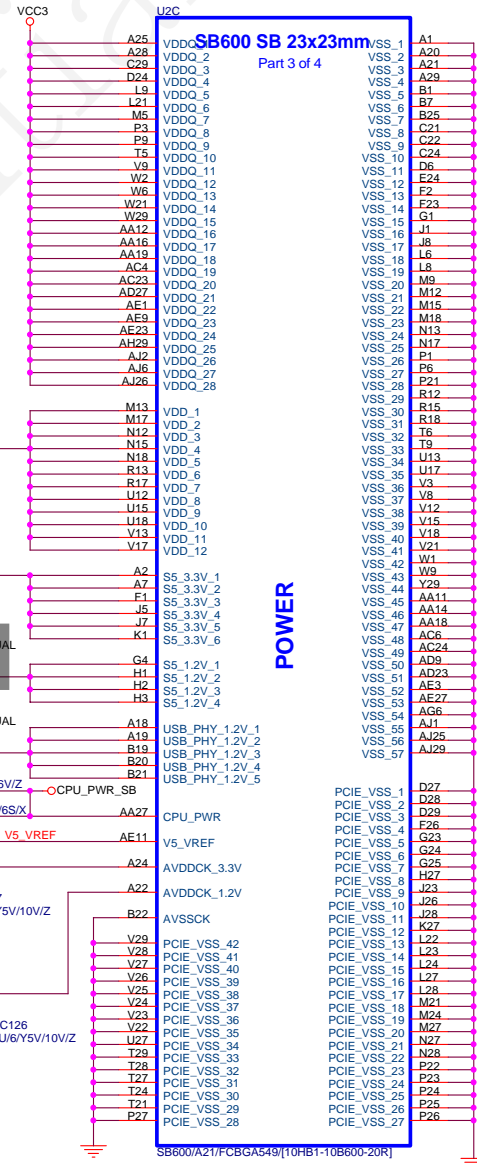
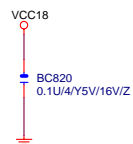
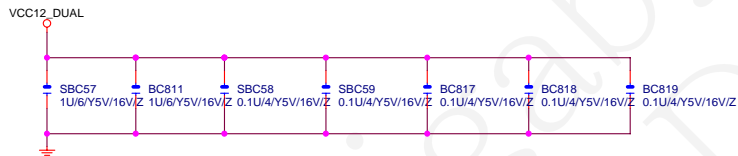
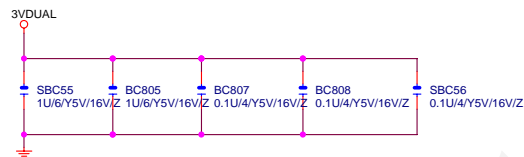
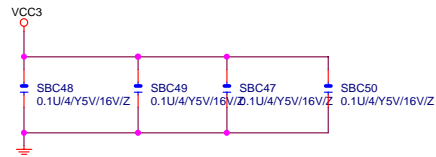
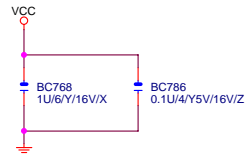
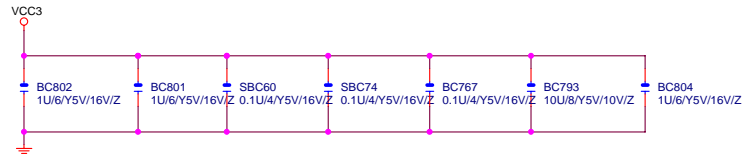
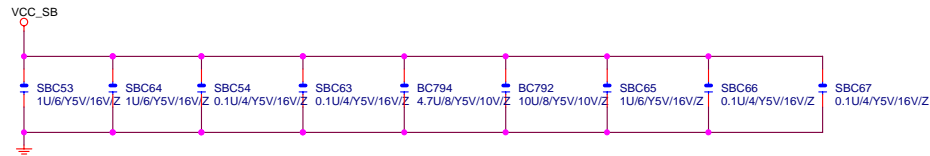
Special NS_VIA CONNECTS
HWM_AGND TO GND at VIA
hole only



NOTE: ROUTE TEMP_COMM
AS A 10MIL TRACE

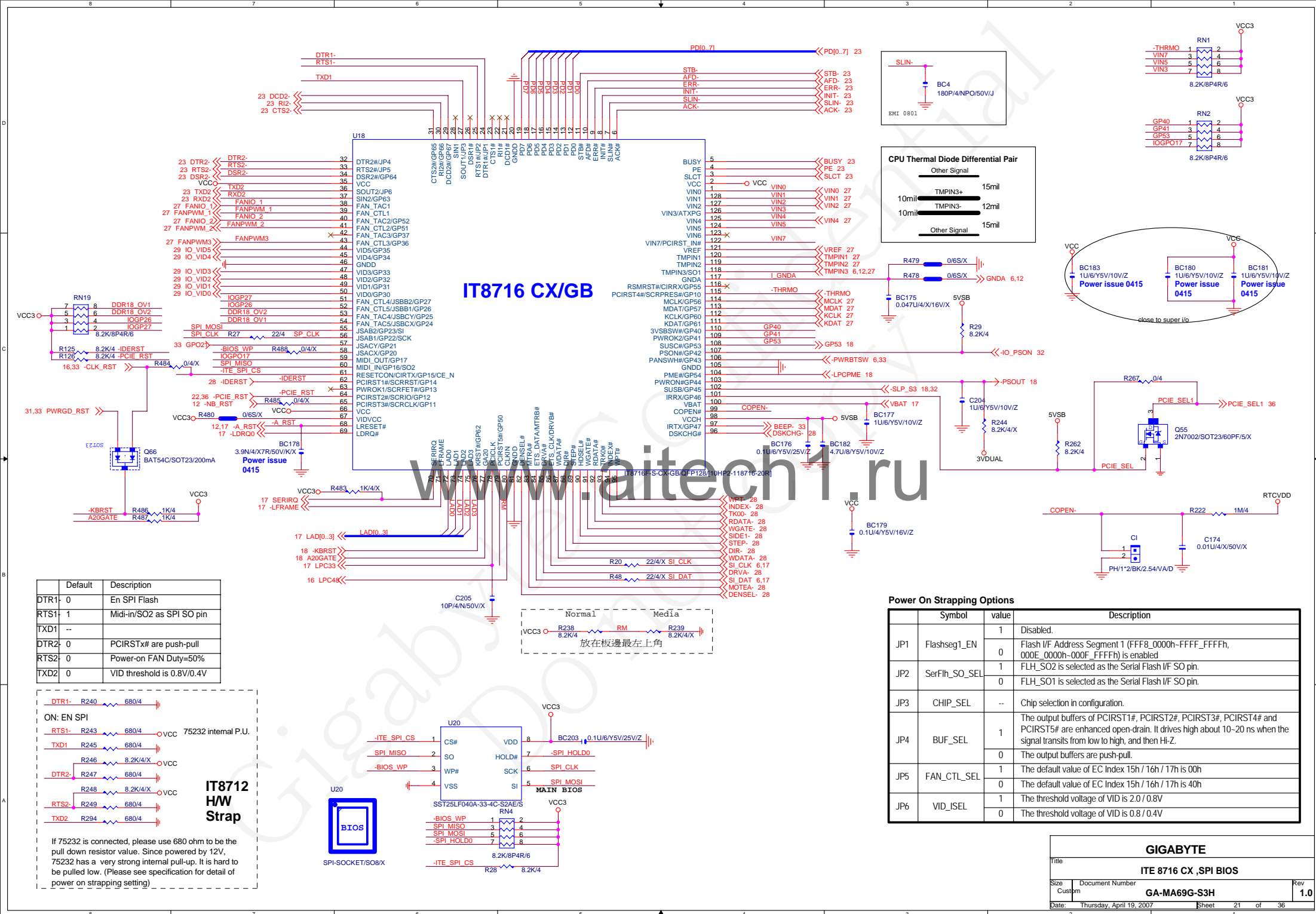
GIGABYTE

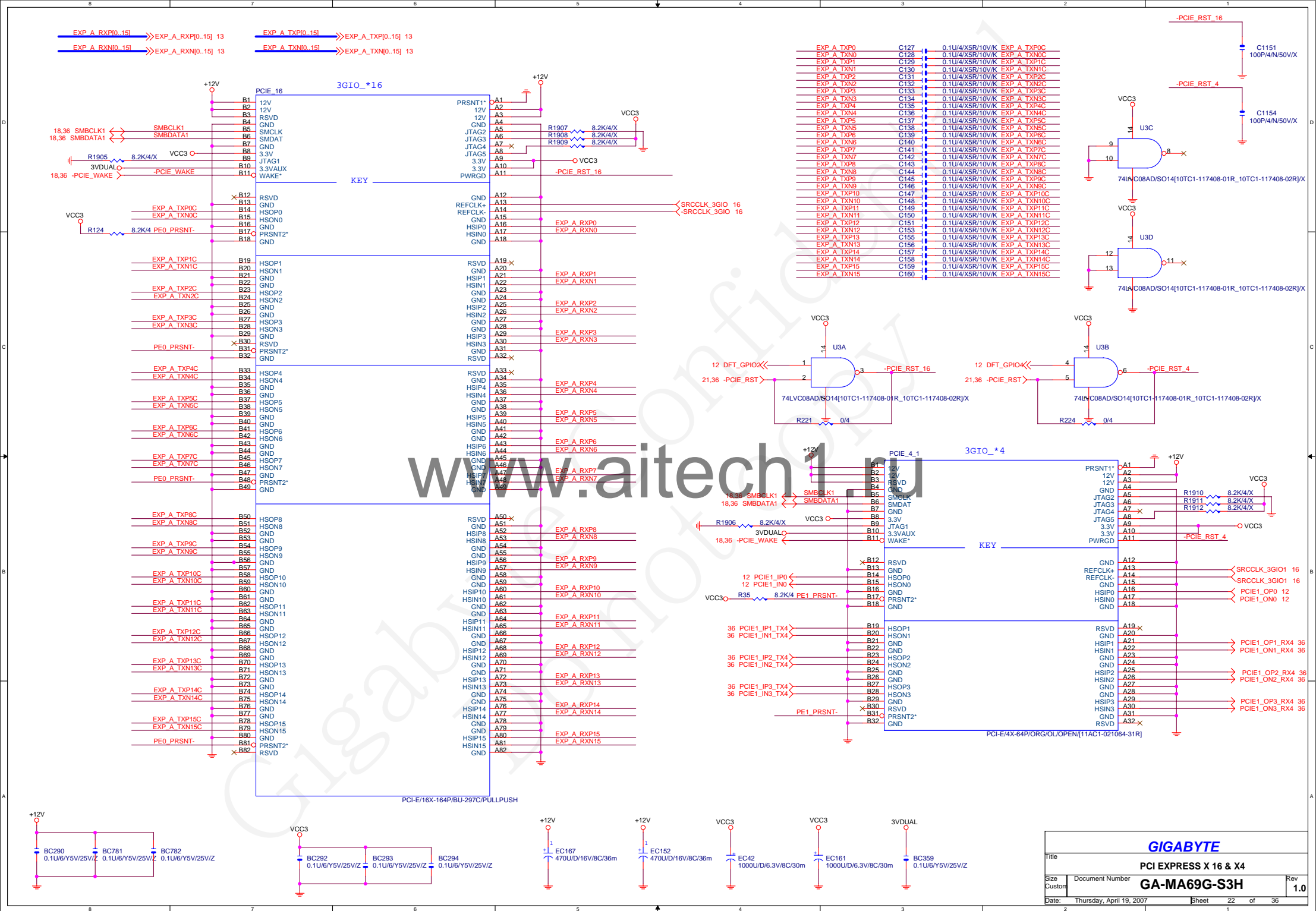
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Size			Document Number	Rev
Custom			GA-MA69G-S3H	1.0
Date:			Thursday, April 19, 2007	Sheet 19 of 36

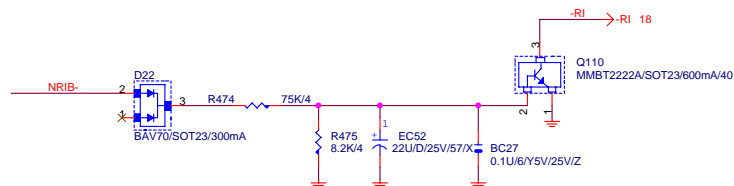


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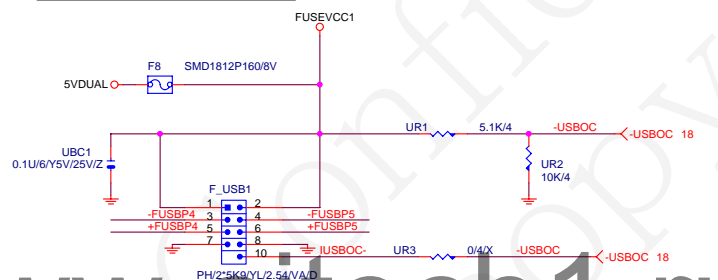
GIGABYTE			
Title			
ATI SB600-PWR/GND			
Size	Document Number	Rev	
Custom	GA-MA69G-S3H	1.0	
Date:	Thursday, April 19, 2007	Sheet	20 of 36



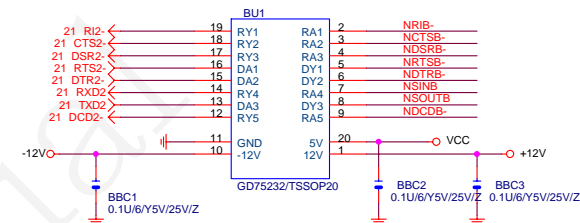
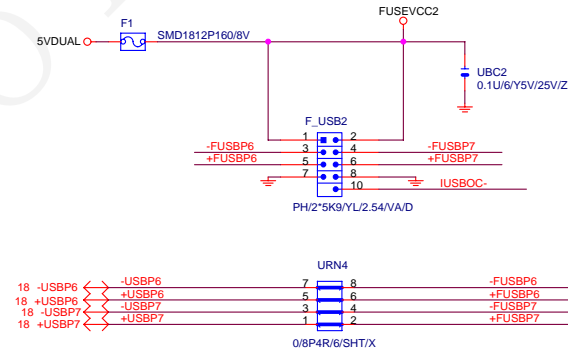




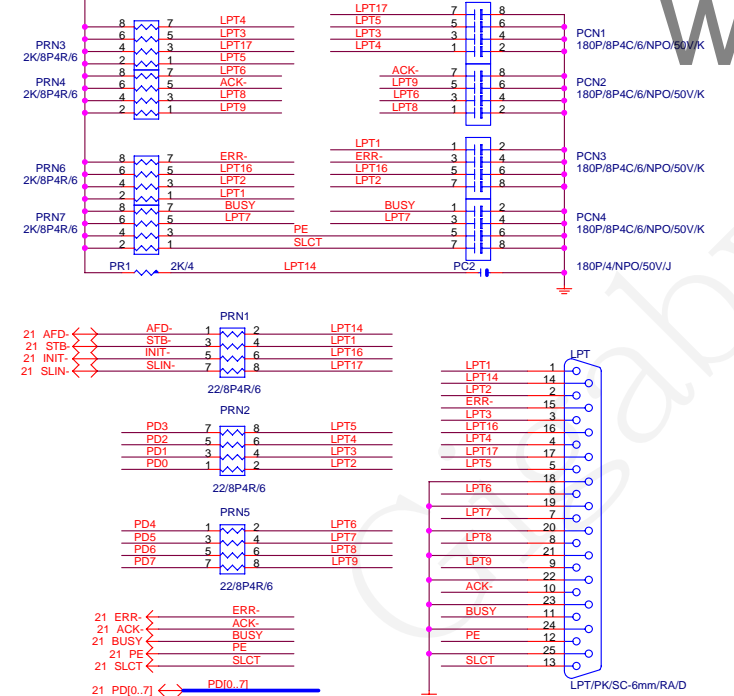
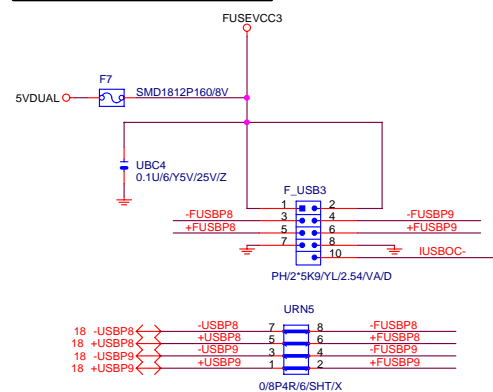
FRONT SIDE USB1



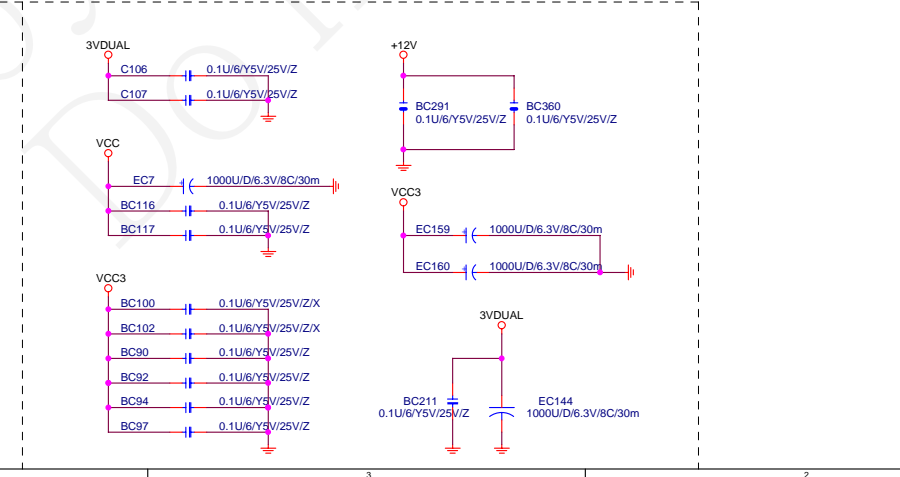
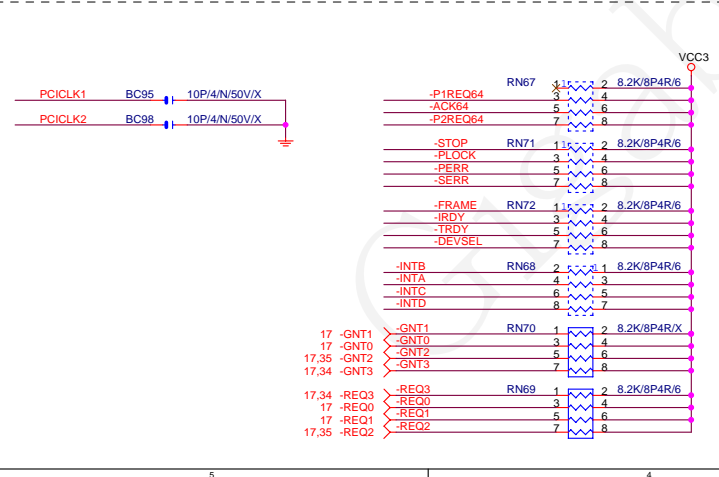
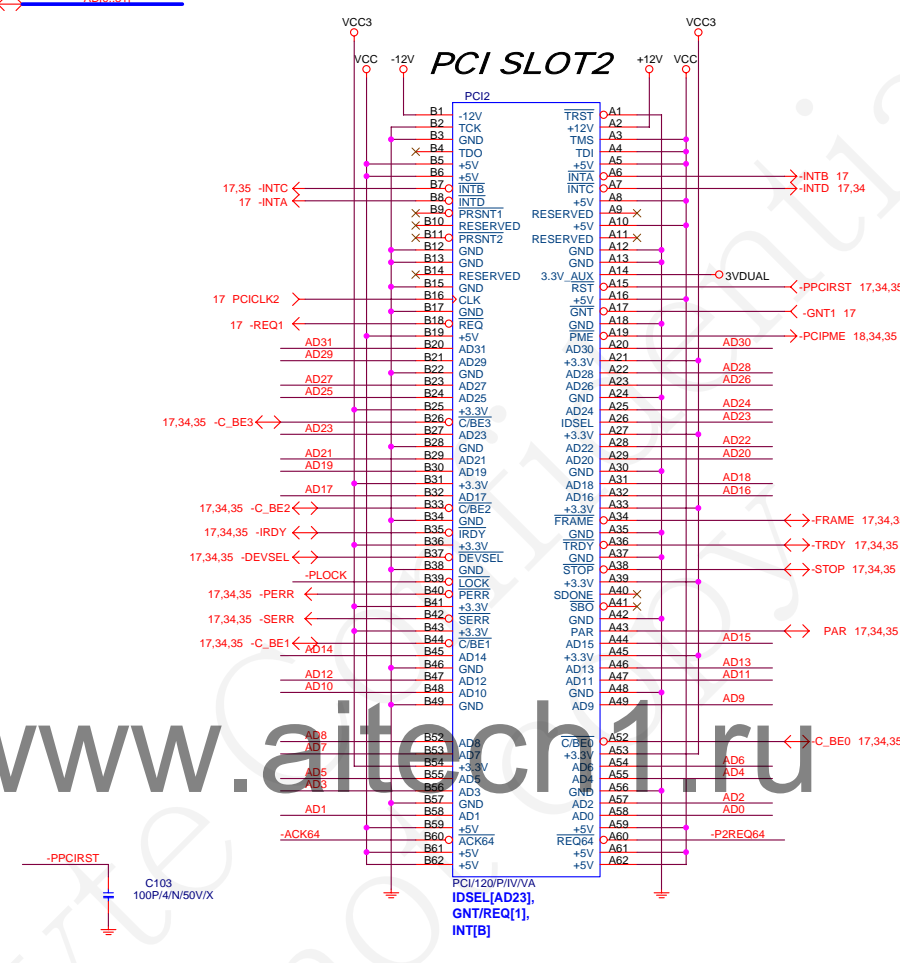
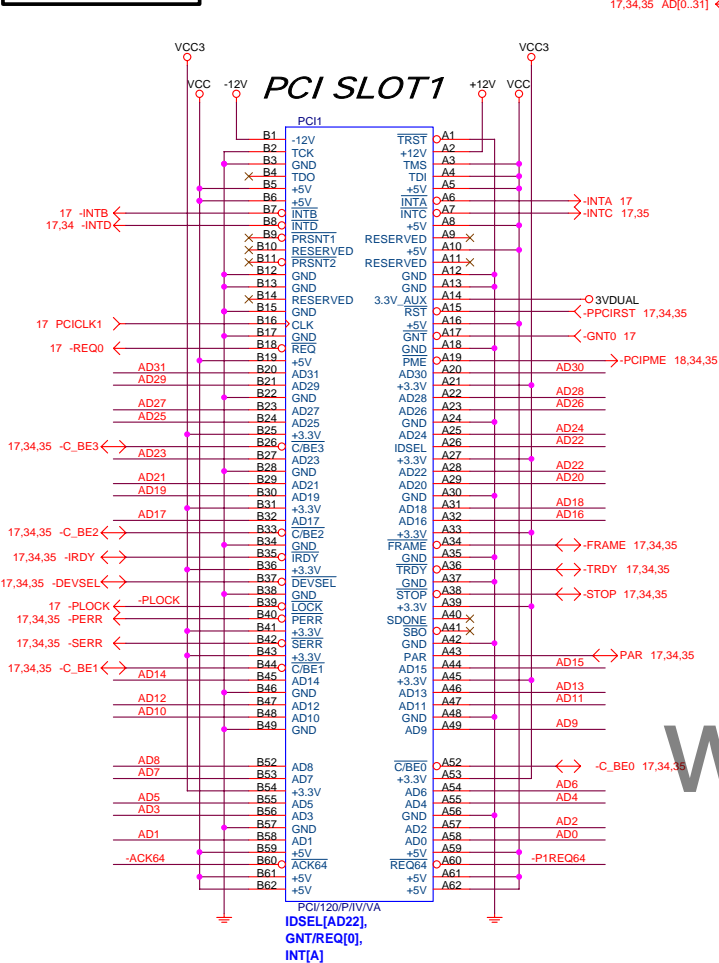
FRONT SIDE USB2

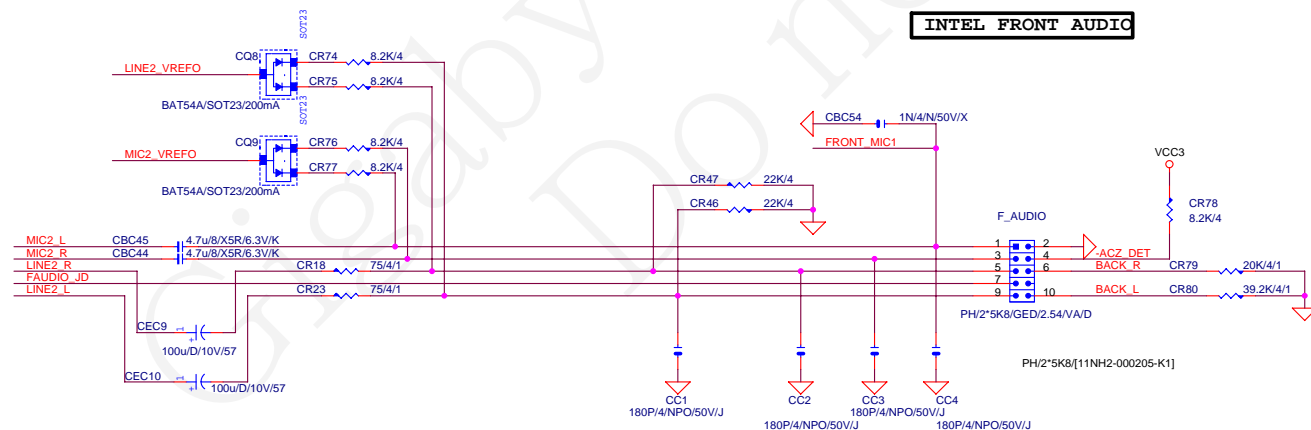
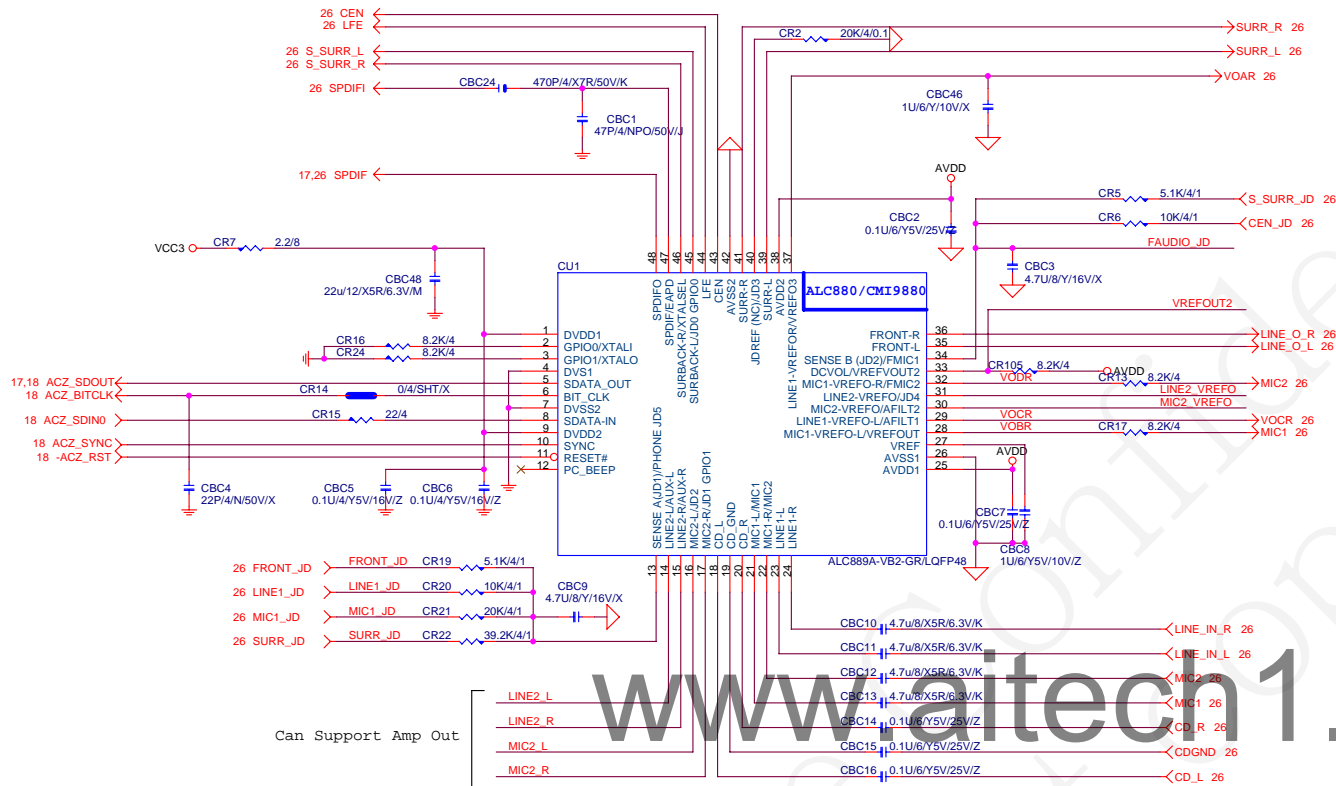


FRONT SIDE USB3



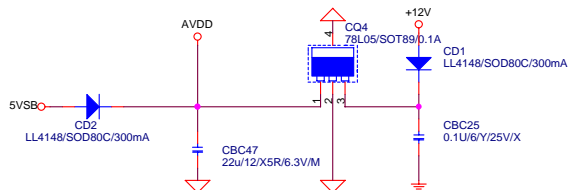
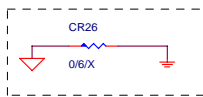
PCI SLOT 1,2,3



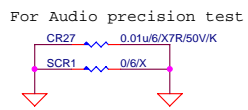
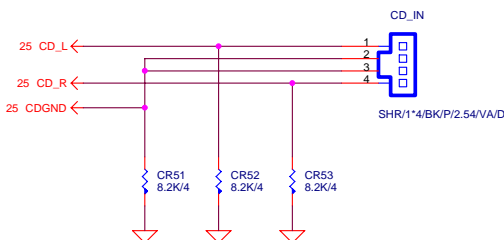


GIGABYTE

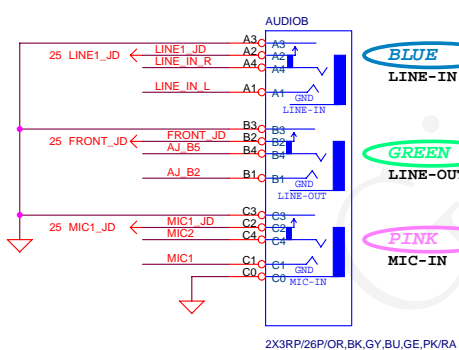
Title			ALC889A CODEC
Size	Document Number	GA-MA69G-S3H	
Custom		Rev	1.0
Date:	Thursday, April 19, 2007	Sheet	25 of 36



CD IN

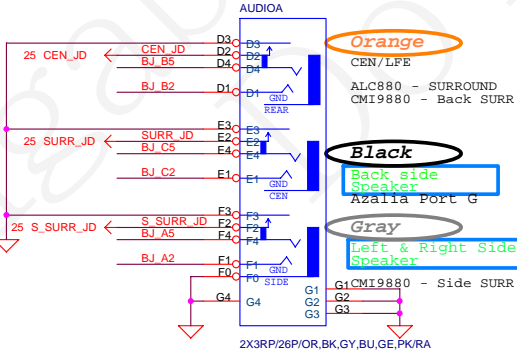
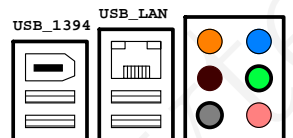
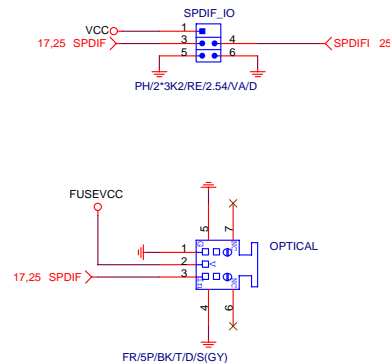


EMI 20070412



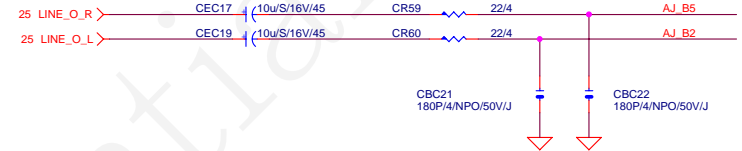
A3RJ/13P/B/[11NR6-403006-01_11NR6-403006-02]
3RJ+15P/[11NR6-403004-11]

SPDIF

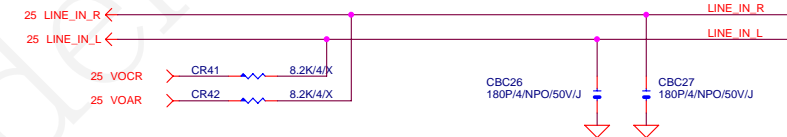


A3RJ/13P/0BG/[11NR6-403006-71]
3RJ+15P/[11NR6-403004-31]

LINE OUT FRONT OUT



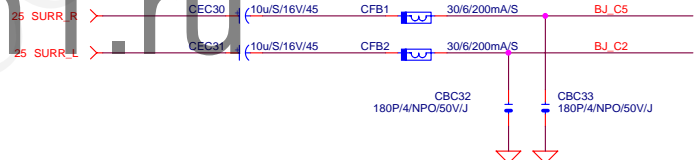
LINE-IN



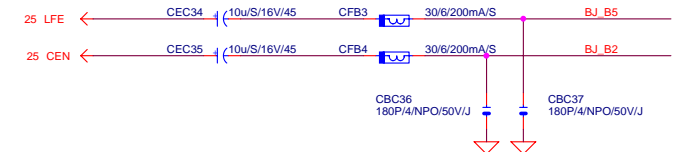
MIC



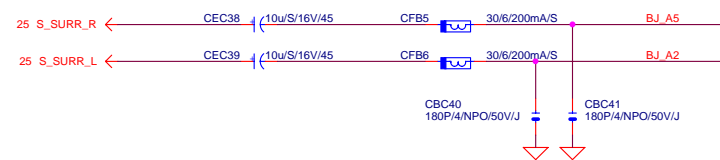
SURROUND



CEN/LFE



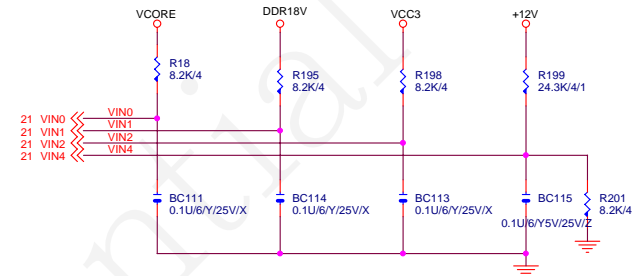
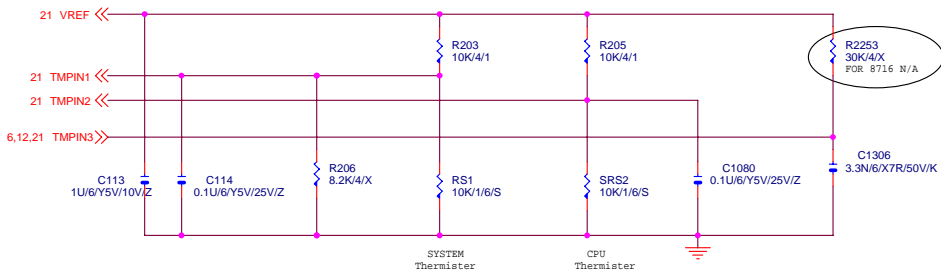
SURR BACK



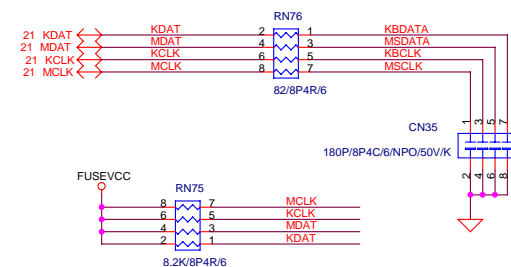
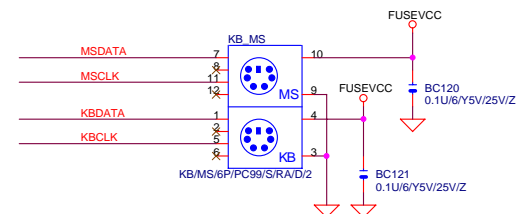
GIGABYTE

Title			
AUDIO JACK			
Size	Document Number	Rev	
Custom	GA-MA69G-S3H	1.0	
Date:	Thursday, April 19, 2007	Sheet	26 of 36

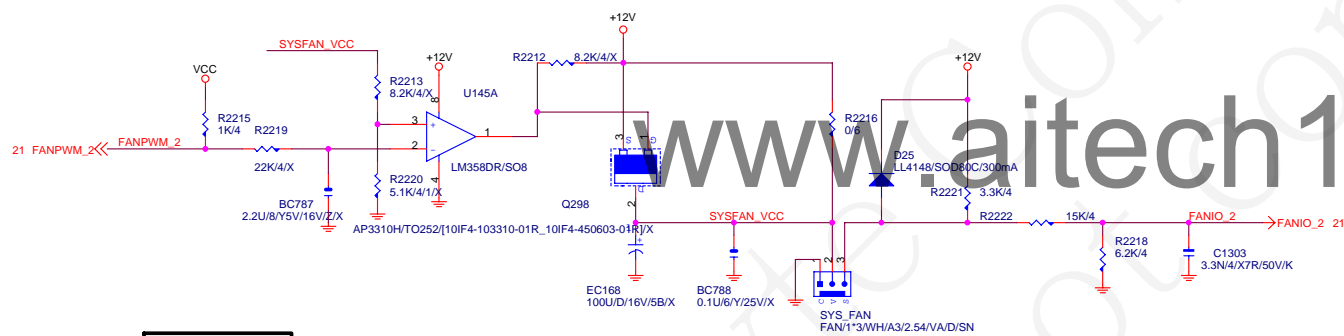
Hardware Monitor circuits



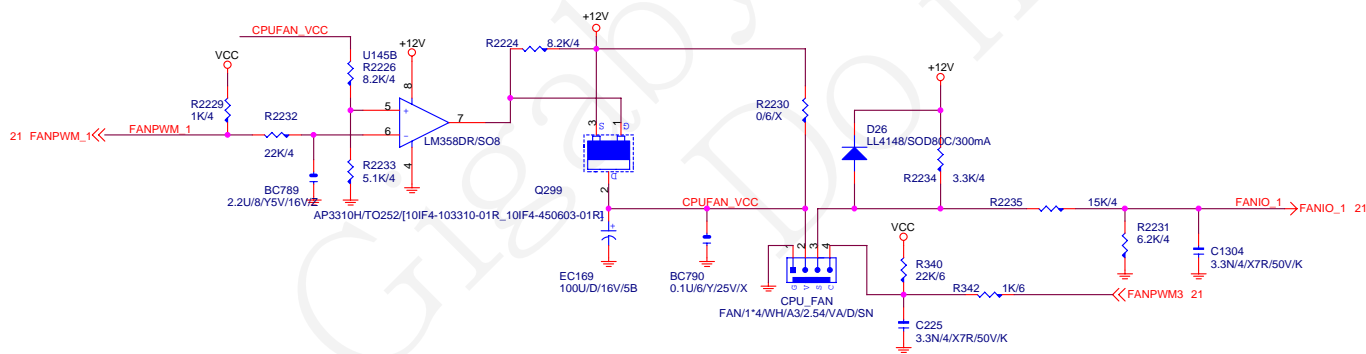
KB & MS



SYSTEM FAN

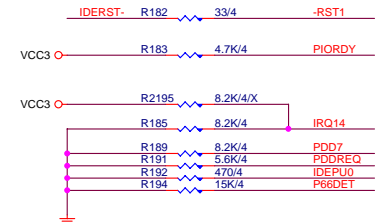
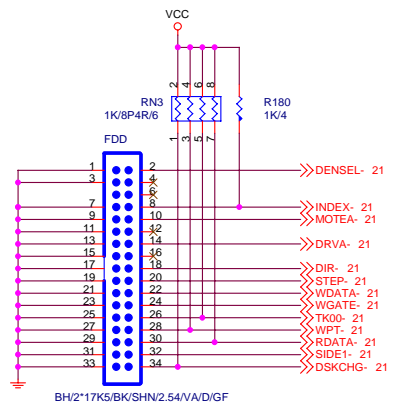


CPU FAN

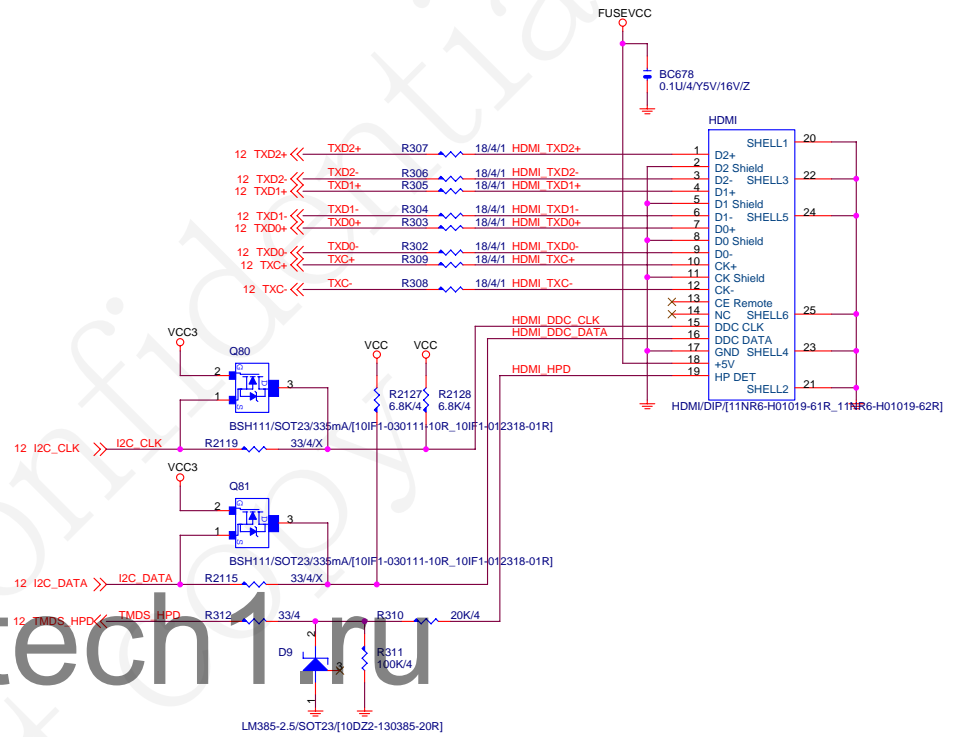
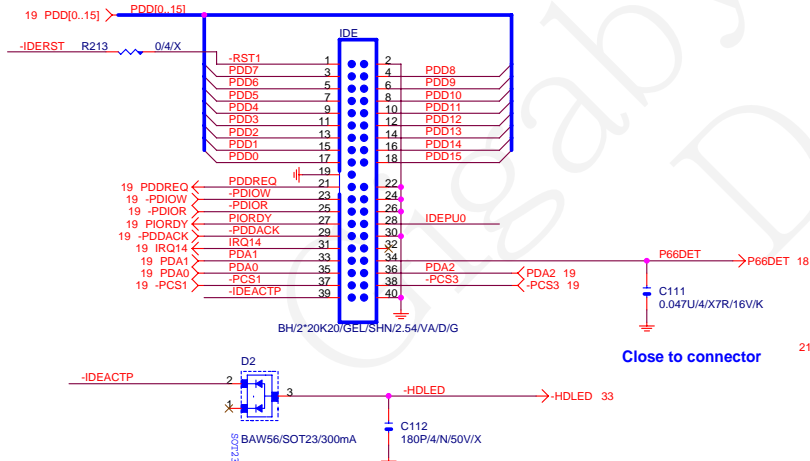


GIGABYTE

Title			
FAN/HWMO			
Size	Document Number		Rev
Custom	GA-MA69G-S3H		1.0
Date:	Thursday, April 19, 2007	Sheet	27 of 36

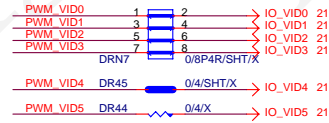
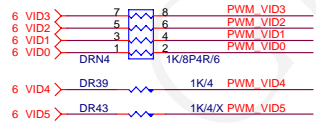
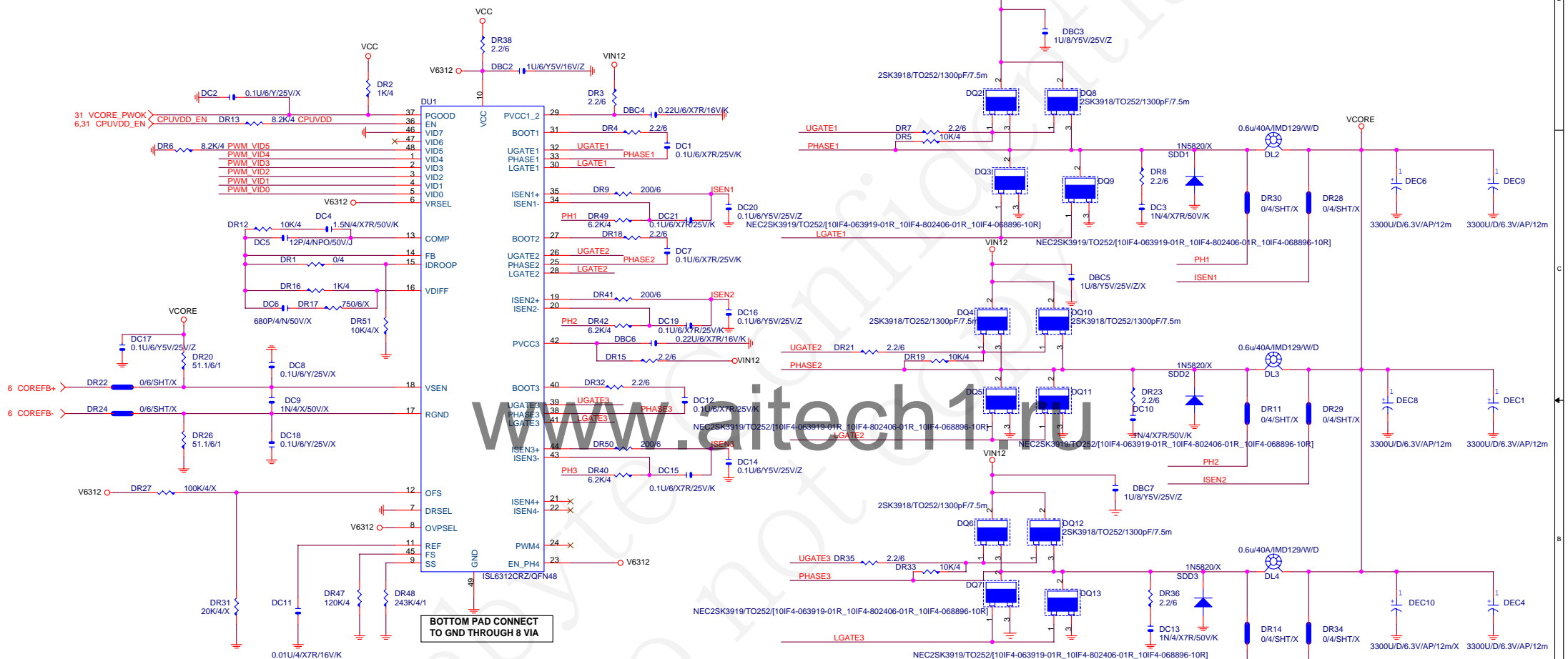
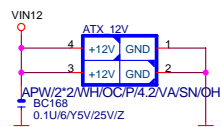
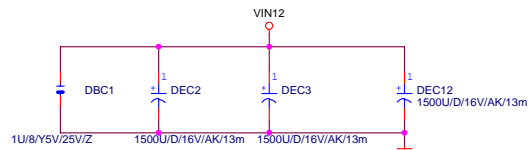


PRIMARY IDE CONNECTOR

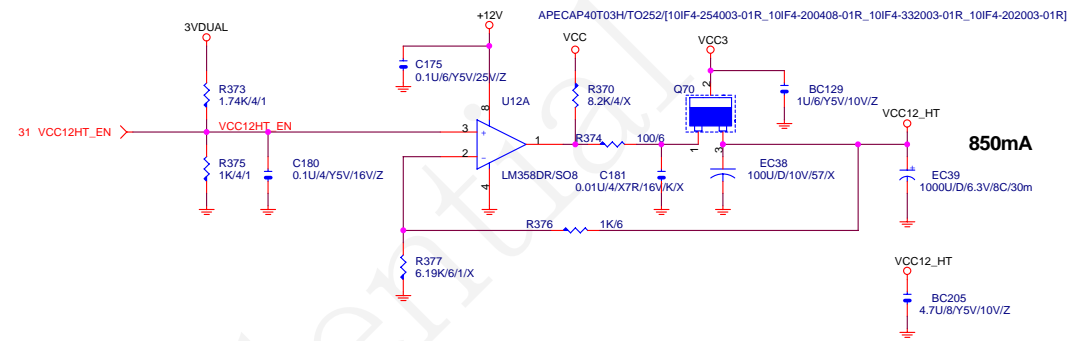


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GIGABYTE			
Title			
IDE/FLOPPY ,HDMI CONNECTOR			
Size	Document Number	Rev	
Custom	GA-MA69G-S3H	1.0	
Date:	Sheet	28	of 36

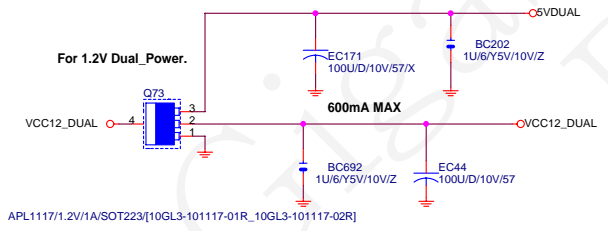
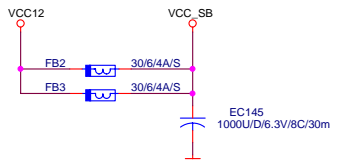


GIGABYTE			
Title			
VCORE PWM ISL6312			
Size	Document Number		Rev
Custom	GA-MA69G-S3H		1.0
Date:	Thursday, April 19, 2007	Sheet	29 of 36



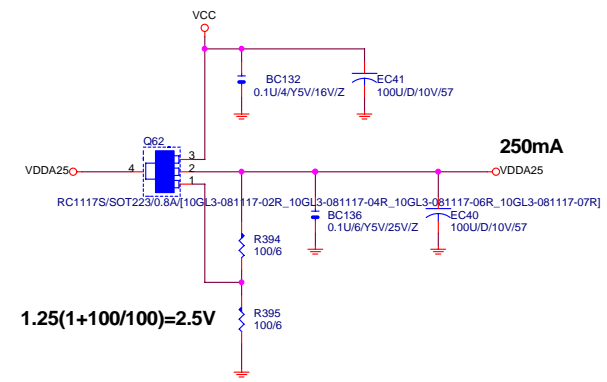
850mA

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For 1.2V Dual_Power.

600mA MAX

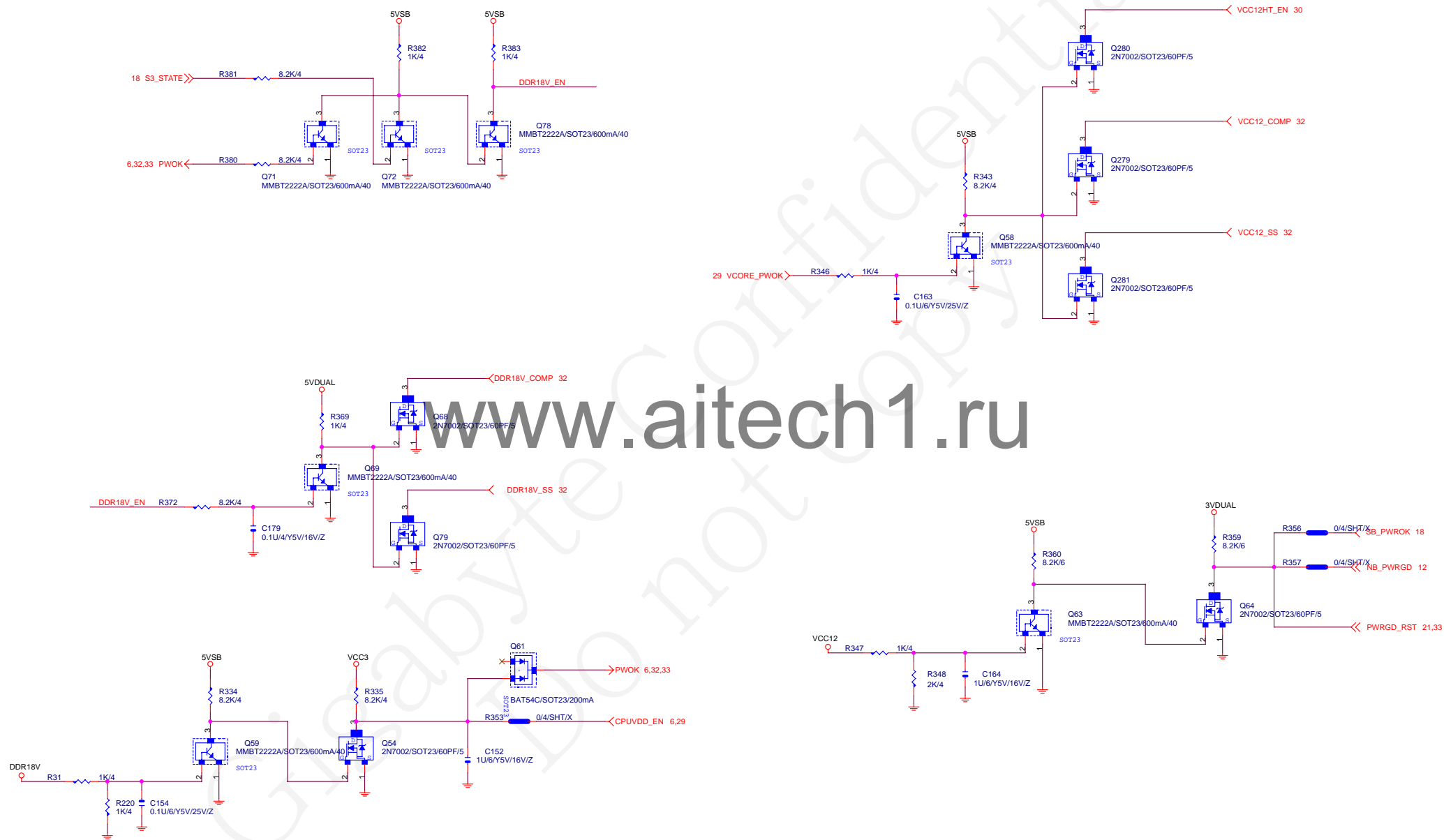


250mA

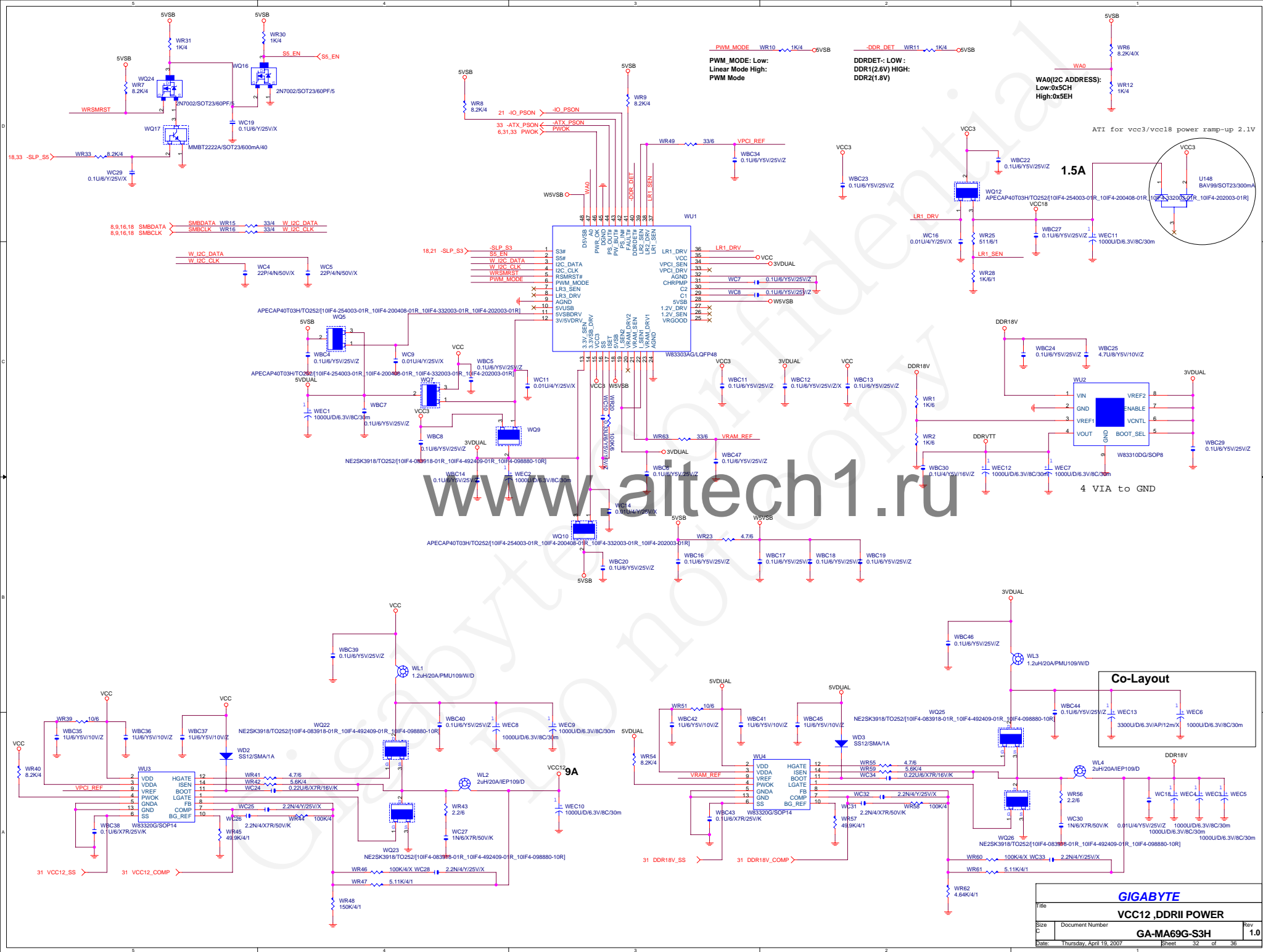
$$1.25(1+100/100)=2.5V$$

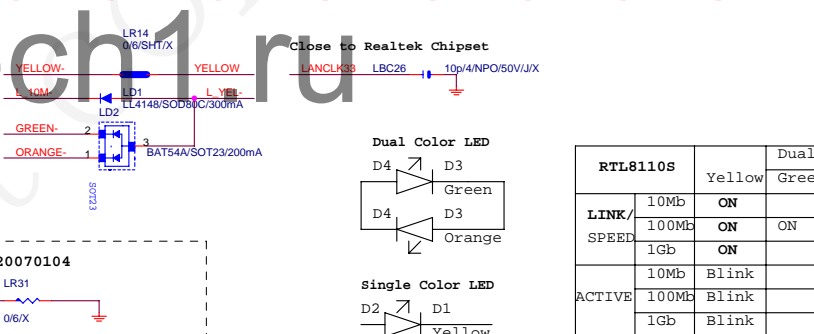
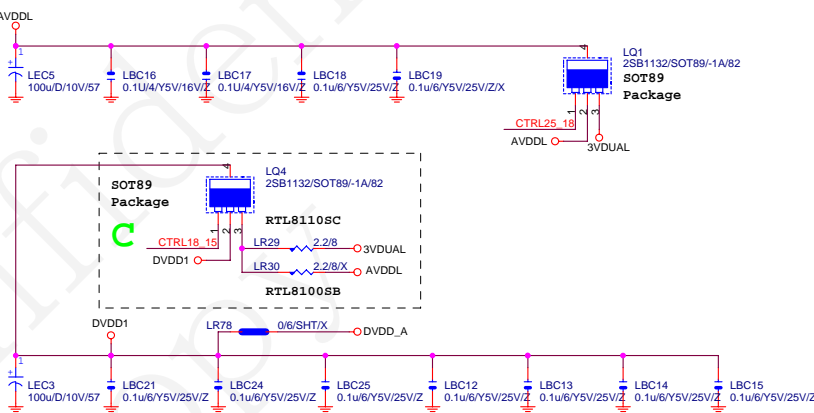
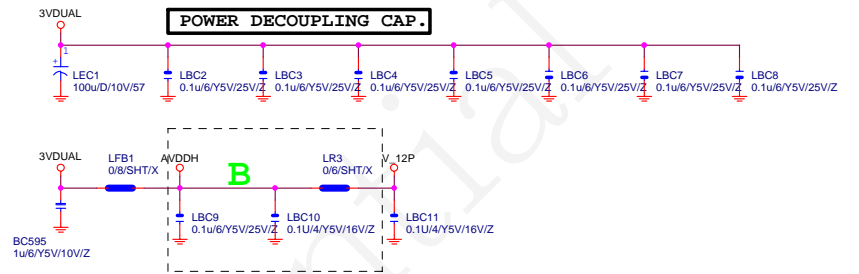
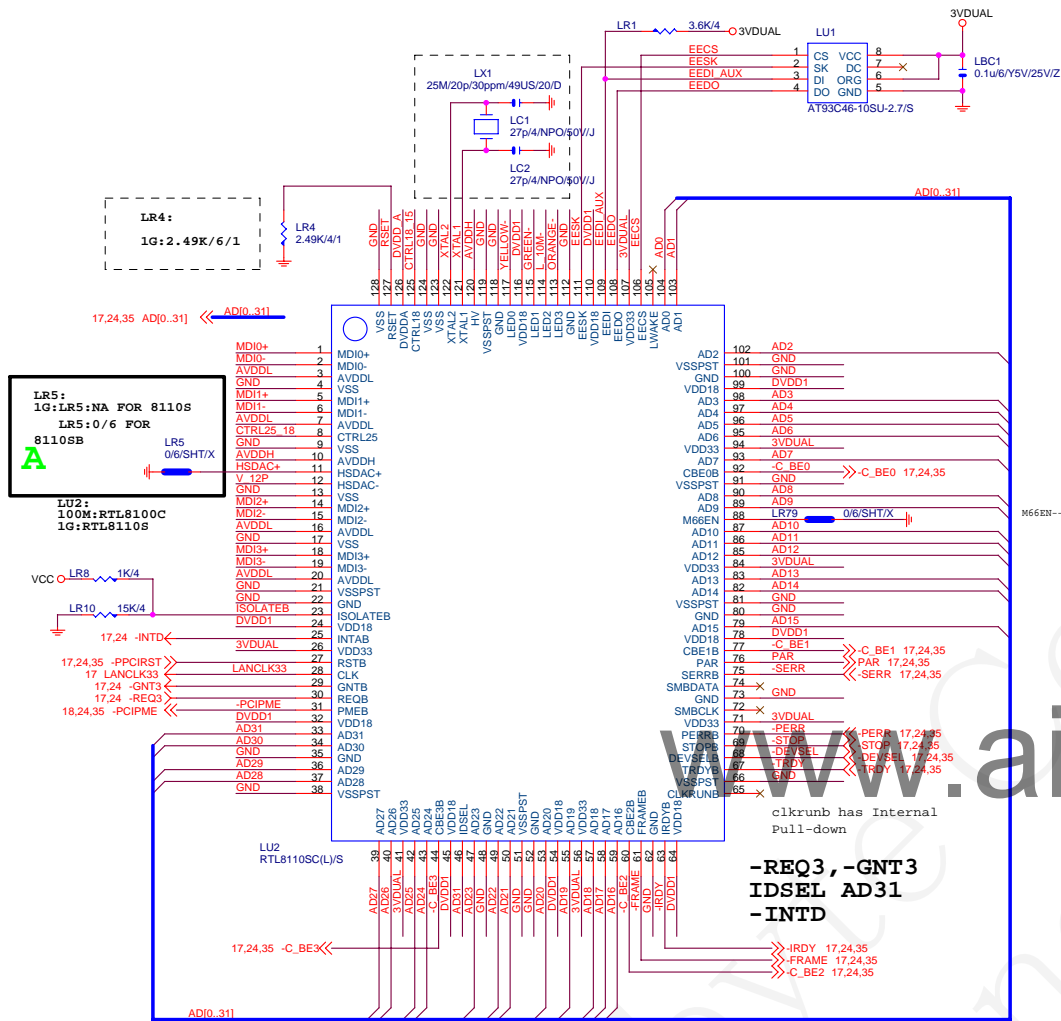
Title		
VCC12HT,VDDA25,VCC12_DUAL POWER		
Size	Document Number	Rev
	GA-MA69G-S3H	1.0
Date:	Thursday, April 19, 2007	Sheet 30 of 36

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Title		
POWER SEQUENCE		
Size	Document Number	Rev
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Date:	Thursday, April 19, 2007	Sheet 31 of 36





RTL8105C	Link/Speed	Dual Color LED		
		Yellow	Green	Orange
10Mb	10Mb	ON		
	100Mb	ON	ON	
	1Gb	ON		ON
100Mb	10Mb	Blink		
	1Gb	Blink		

	RTL8100C	RTL8110S/RTL8169S	RTL8110SB/RTL8169SB	RTL8110SC
AVDDH	N/A	3.3AVDD	3.3AVDD	3.3AVDD
V_12P	2.5AVDD	N/A	3.3AVDD	3.3AVDD
AVDDL	3.3AVDD	2.5AVDD	2.5AVDD	1.8AVDD
V_DAC	N/A	2.5AVDD	2.5AVDD	N/A
DVDD1	2.5VDD	1.8VDD	1.2VDD	1.5VDD
DVDD_A	N/A	1.8AVDD	1.2AVDD	1.5AVDD

